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Fig.1

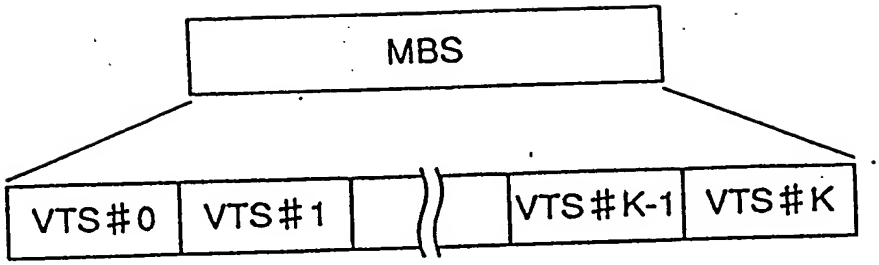


Fig.2

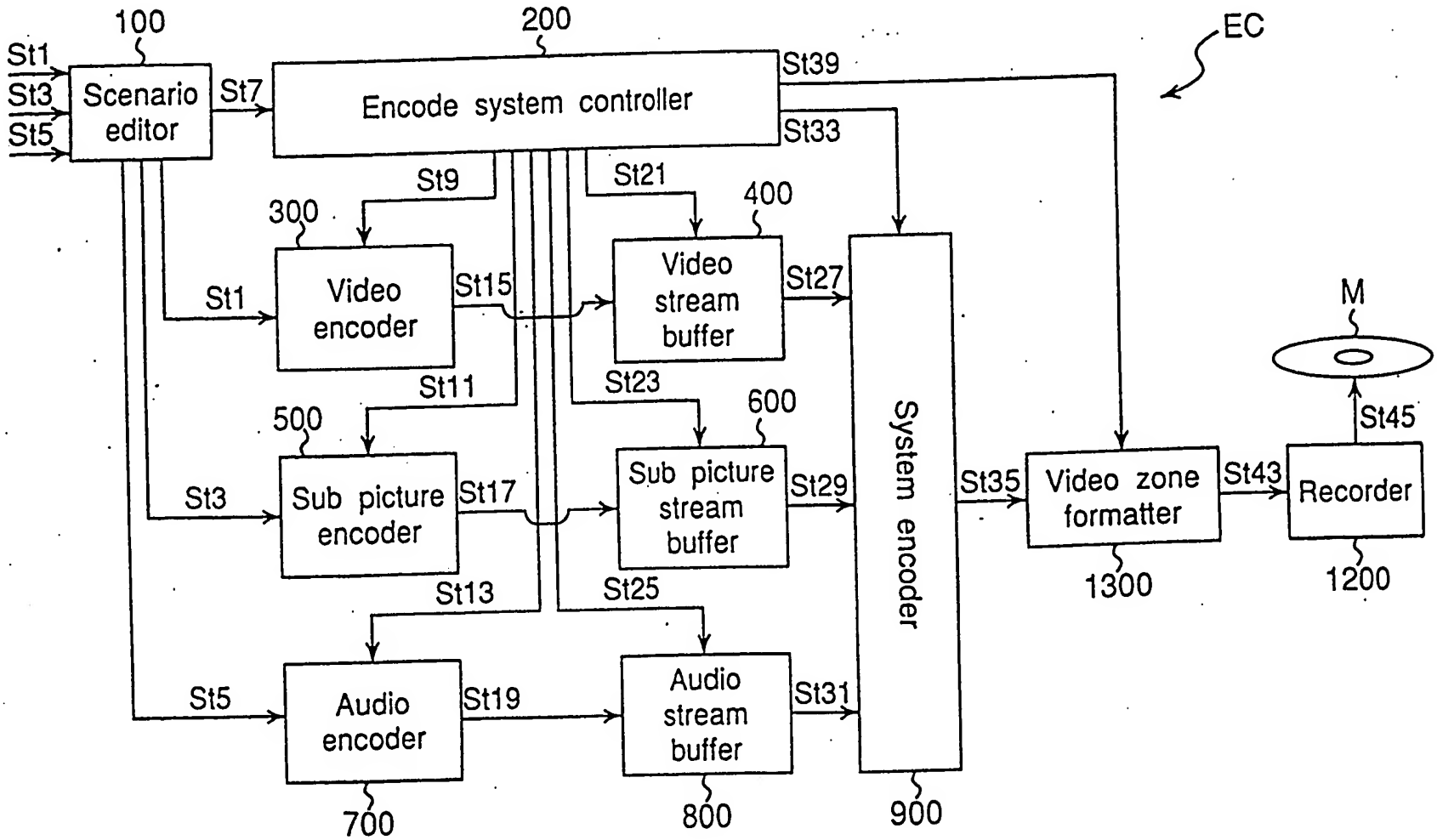

100
200
EC
St1
St3
St5
Scenario editor
St7
Encode system controller
St39
St33
St9
St21
300
400
Video encoder
St15
Video stream buffer
St27
St1
M
St11
St23
500
600
St45
Sub picture encoder
St17
Sub picture stream buffer
St29
St3
St35
Video zone formatter
St43
Recorder
System encoder
St13
St25
1300
1200
St5
Audio encoder
St19
Audio stream buffer
St31
700
800
900

Fig.3

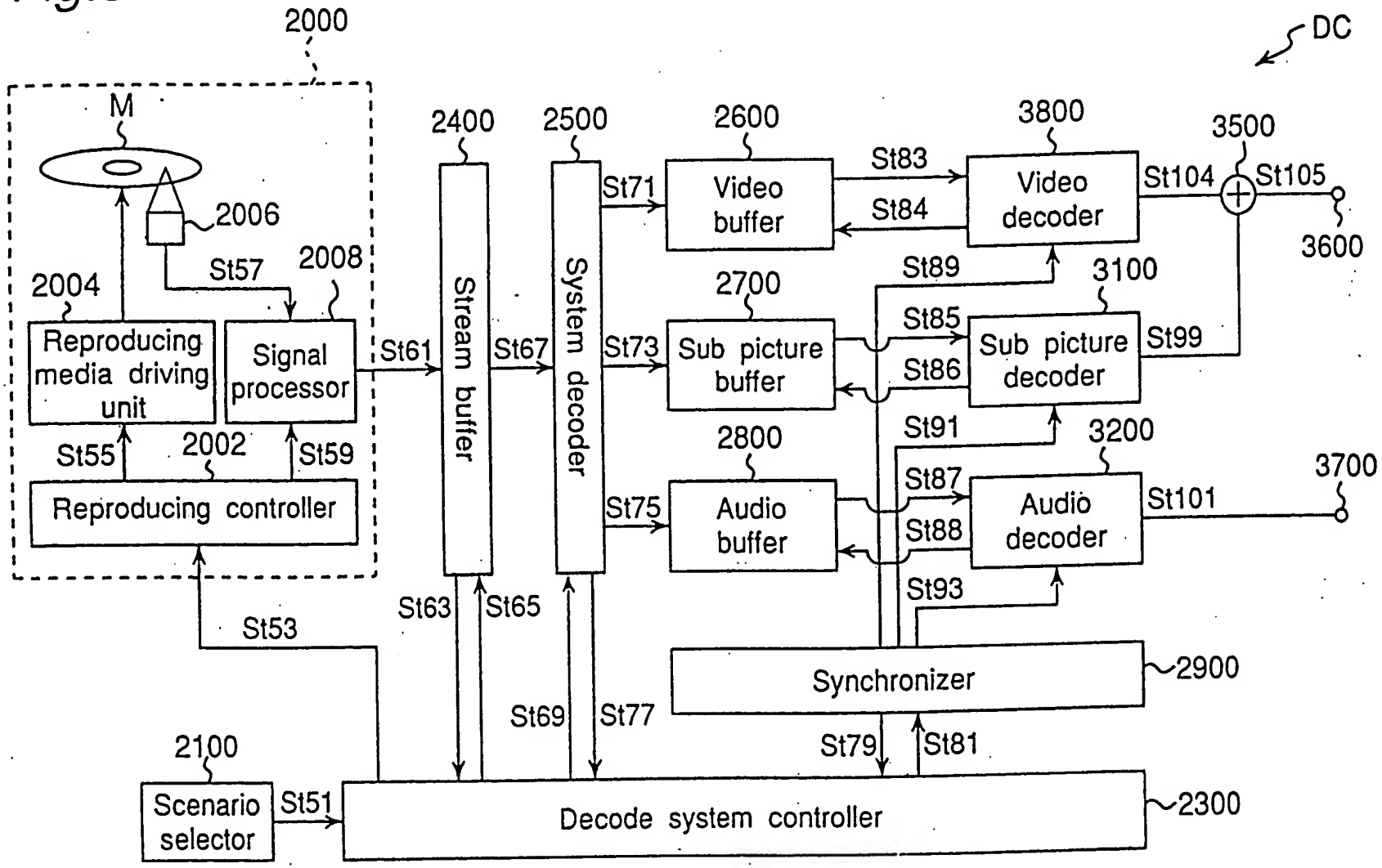

2000
DC
M
2006
2004
St57
2008
Reproducing media driving unit
Signal processor
St61
St55
2002
St59
Reproducing controller
St53
2400
Stream buffer
St63
St65
2500
System decoder
St67
St69
St77
2100
Scenario selector
St51
Decode system controller
2300
St71
2600
Video buffer
St73
2700
Sub picture buffer
St75
2800
Audio buffer
St79
St81
Synchronizer
2900
St83
St84
3800
Video decoder
St89
St85
St86
3100
Sub picture decoder
St91
St87
St88
3200
Audio decoder
St93
3500
St104
St105
3600
St99
St101
3700

Fig.4

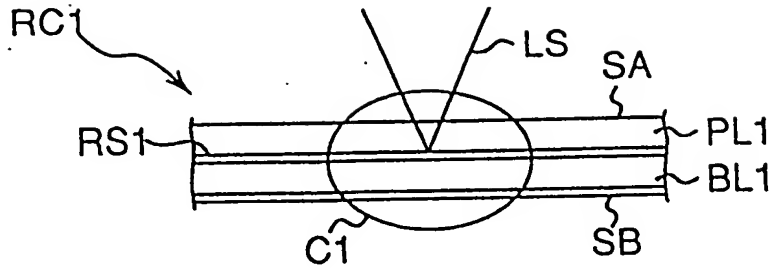


Fig.5

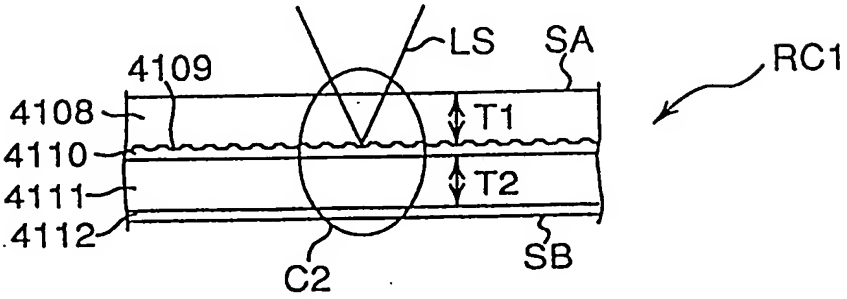


Fig.6

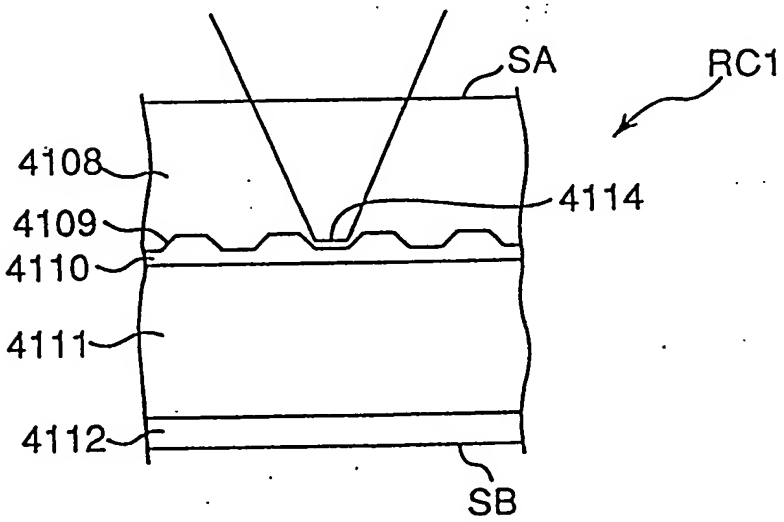


Fig.7

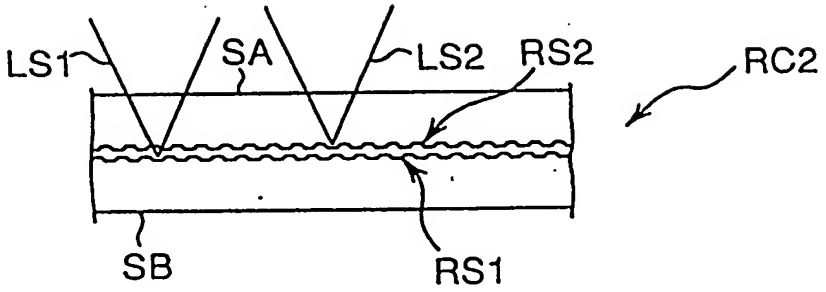


Fig.8

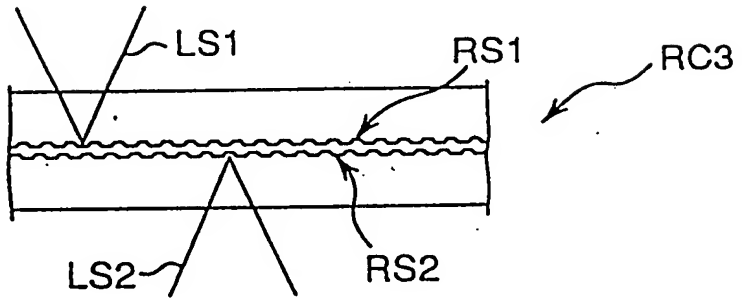


Fig.9

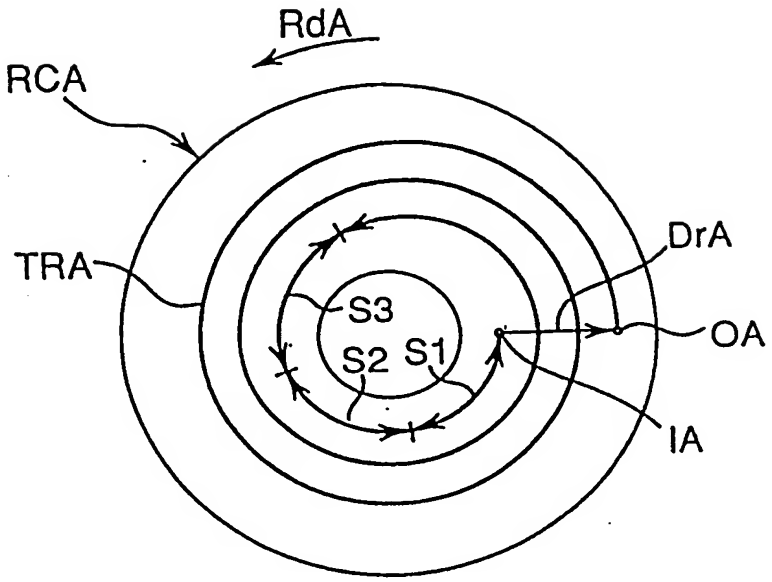


Fig.10

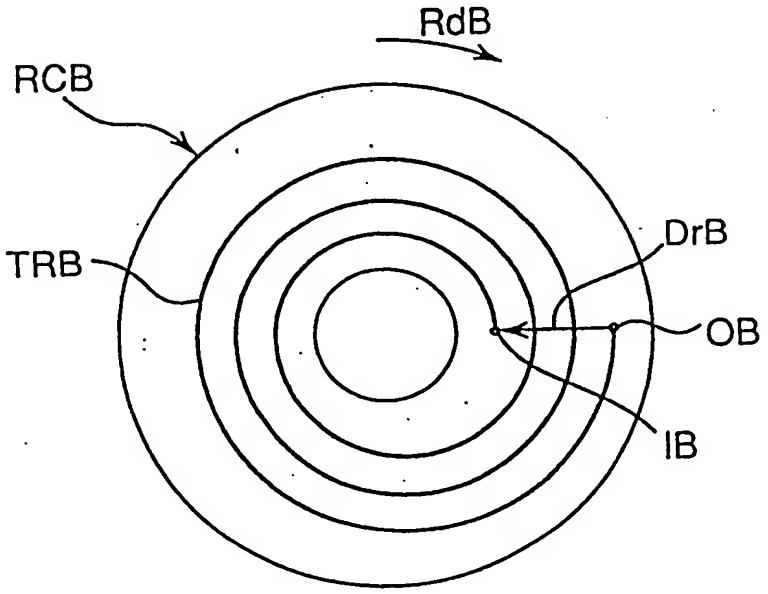


Fig.11

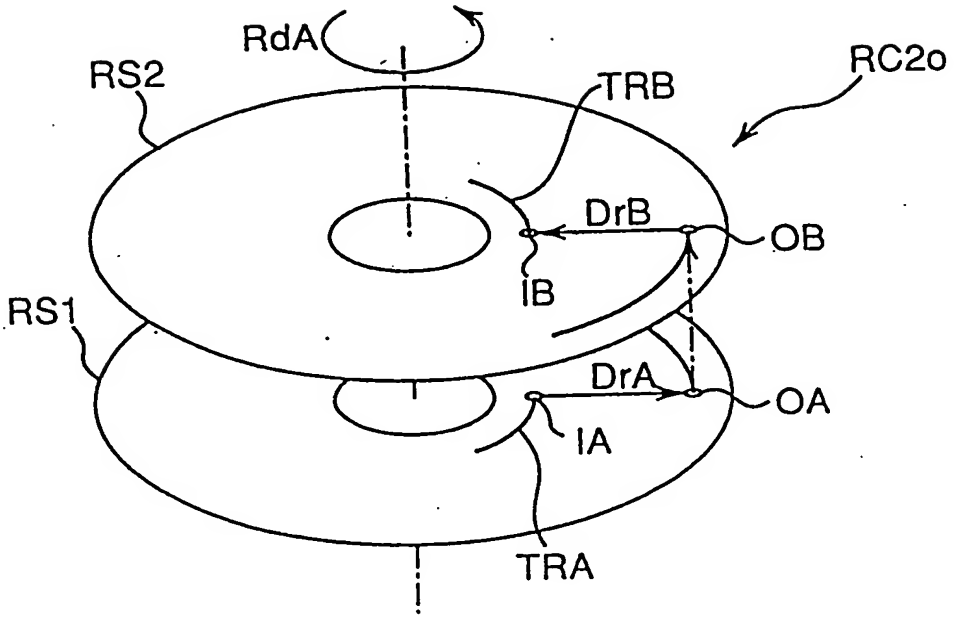

RdA
RS2
TRB
RC2o
DrB
OB
IB
RS1
DrA
OA
IA
TRA

Fig.12

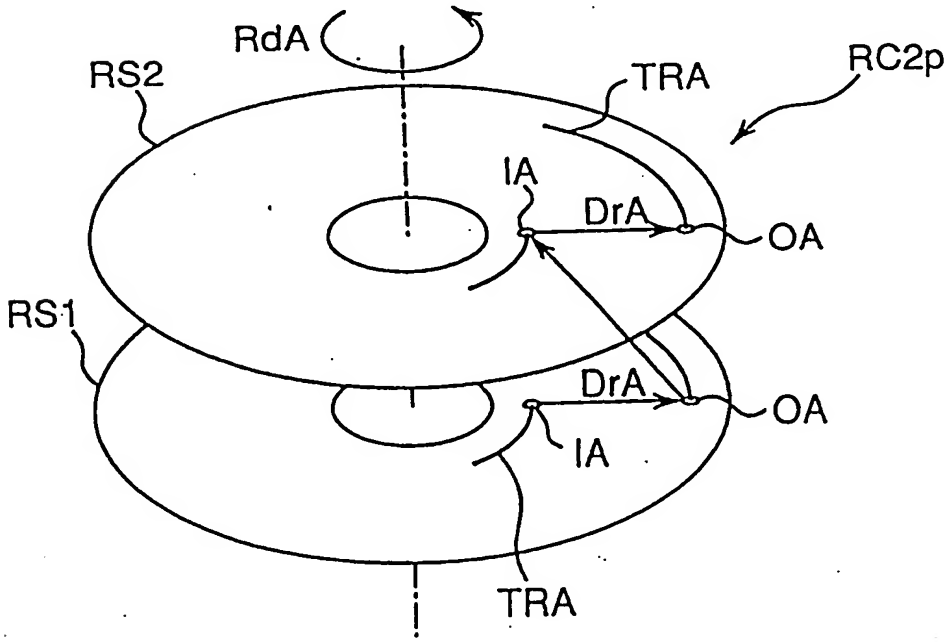

RdA
RS2
TRA
RC2p
IA
DrA
OA
RS1
DrA
OA
IA
TRA

Fig.13

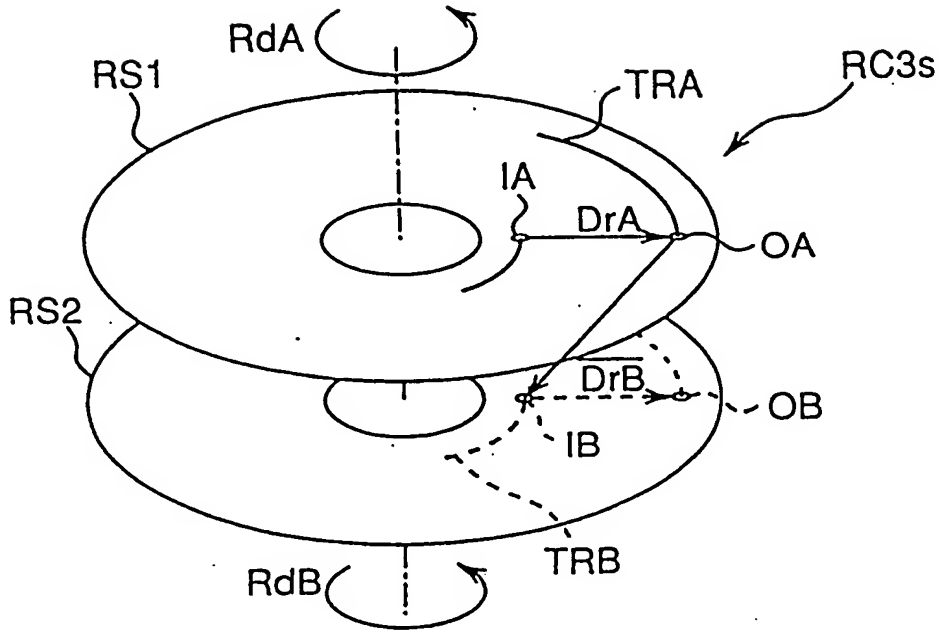

RdA
RS1
TRA
RC3s
IA
DrA
OA
RS2
DrB
OB
IB
TRB
RdB

Fig.14

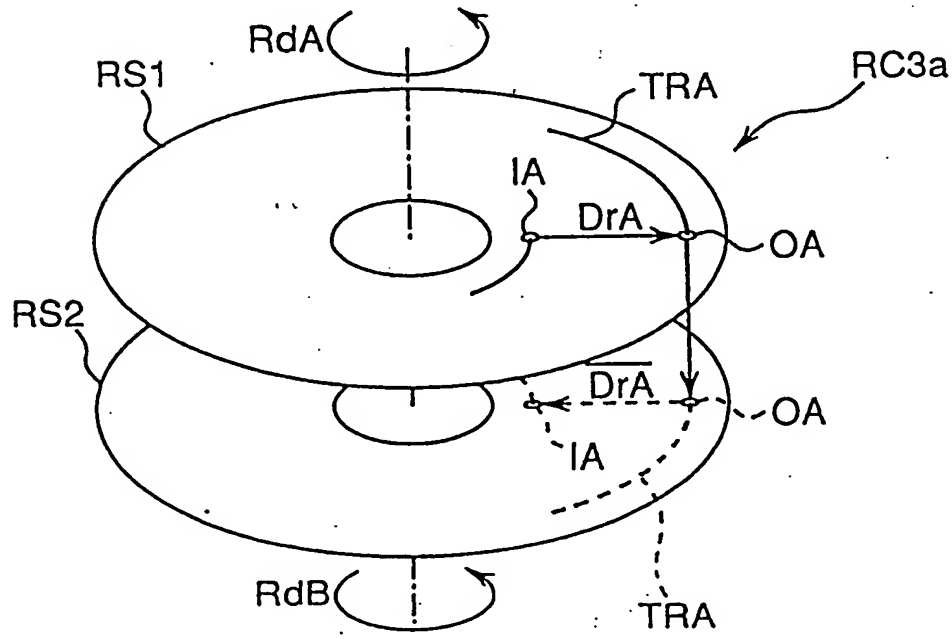

RdA
RS1
TRA
RC3a
IA
DrA
OA
RS2
DrA
OA
IA
RdB
TRA

Fig.15

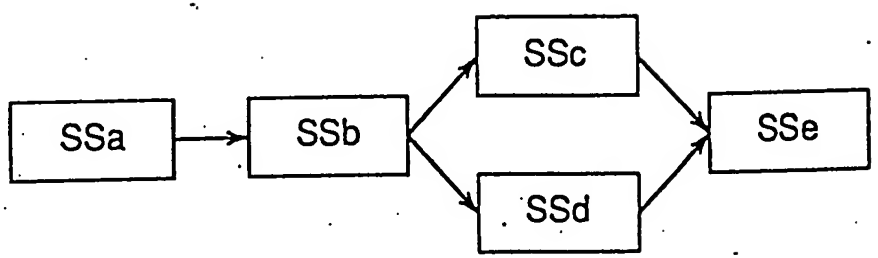


Fig.16

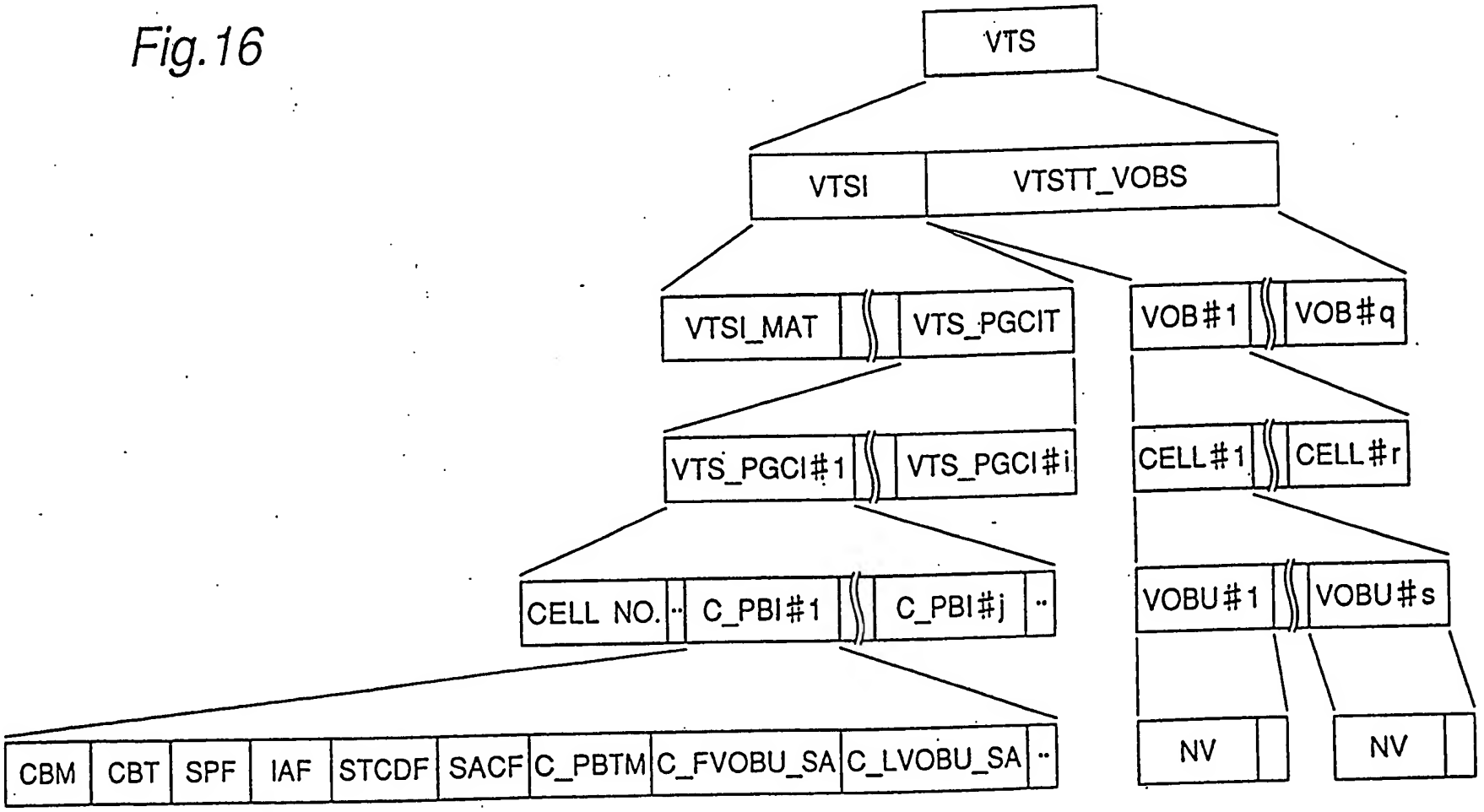

VTS
VTSI
VTSTT_VOBS
VTSI_MAT
VTS_PGCIT
VOB#1
VOB#q
VTS_PGCI#1
VTS_PGCI#i
CELL#1
CELL#r
CELL NO.
C_PBI#1
C_PBI#j
VOBU#1
VOBU#s
CBM
CBT
SPF
IAF
STCDF
SACF
C_PBTM
C_FVOBU_SA
C_LVOBU_SA
NV
NV

Fig.17

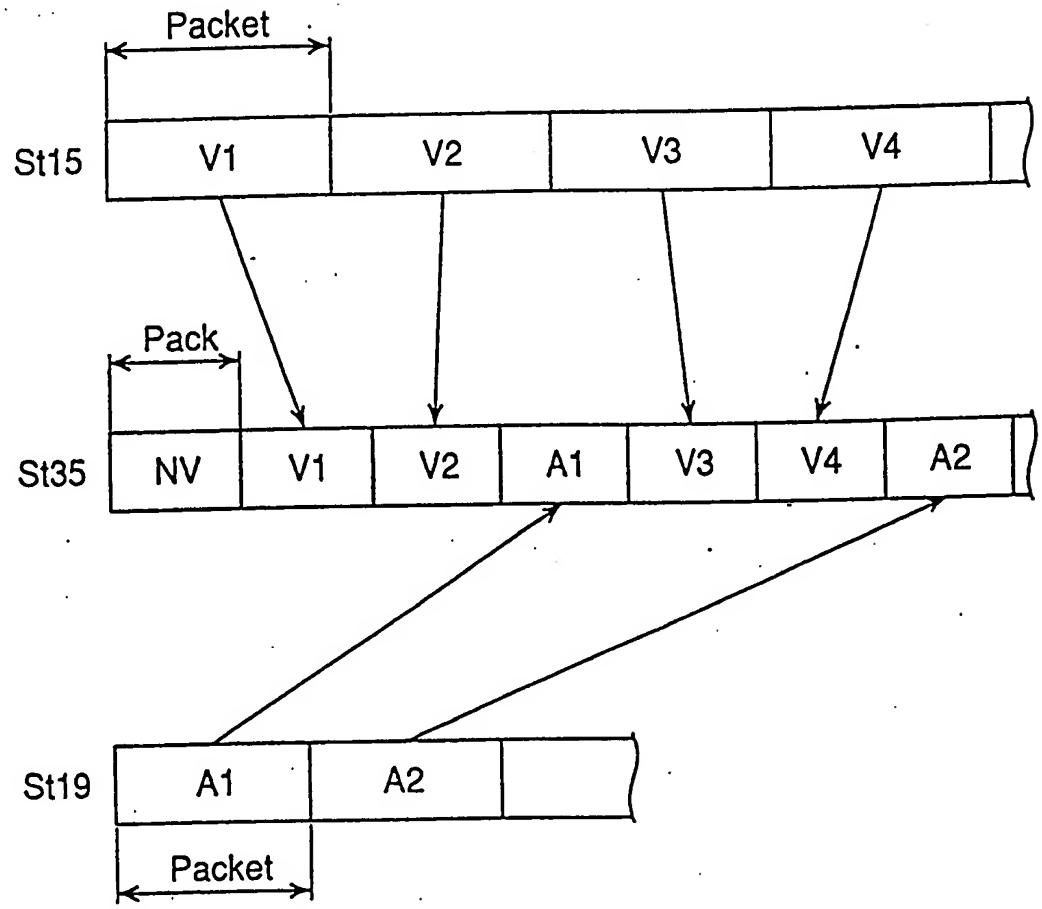

Packet
St15
V1
V2
V3
V4
Pack
St35
NV
V1
V2
A1
V3
V4
A2
St19
A1
A2
Packet

Fig.18

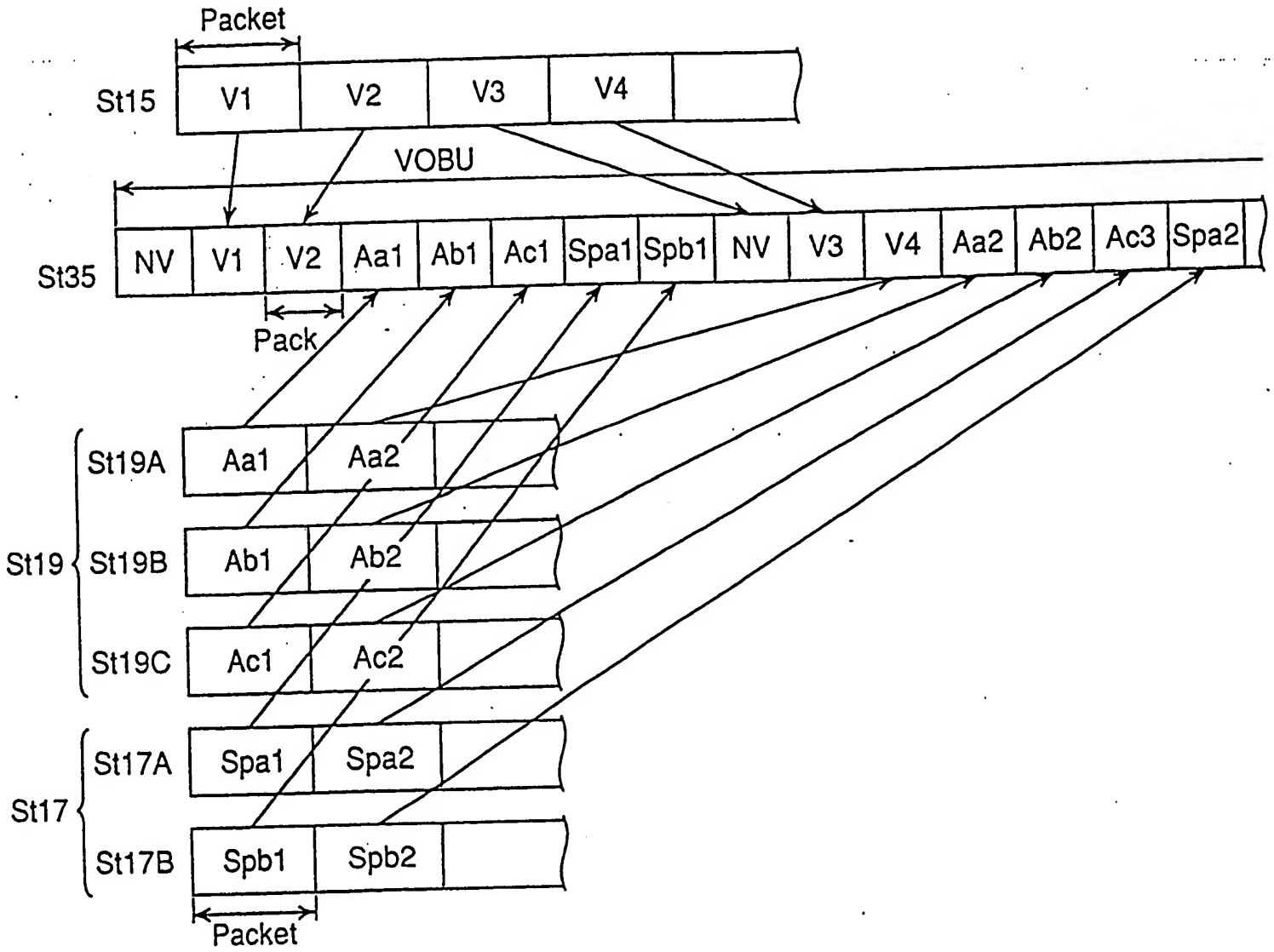

Packet
St15
V1
V2
V3
V4
VOBU
St35
NV
V1
V2
Aa1
Ab1
Ac1
Spa1
Spb1
NV
V3
V4
Aa2
Ab2
Ac3
Spa2
Pack
St19
St19A
Aa1
Aa2
St19B
Ab1
Ab2
St19C
Ac1
Ac2
St17
St17A
Spa1
Spa2
St17B
Spb1
Spb2
Packet

Fig.19

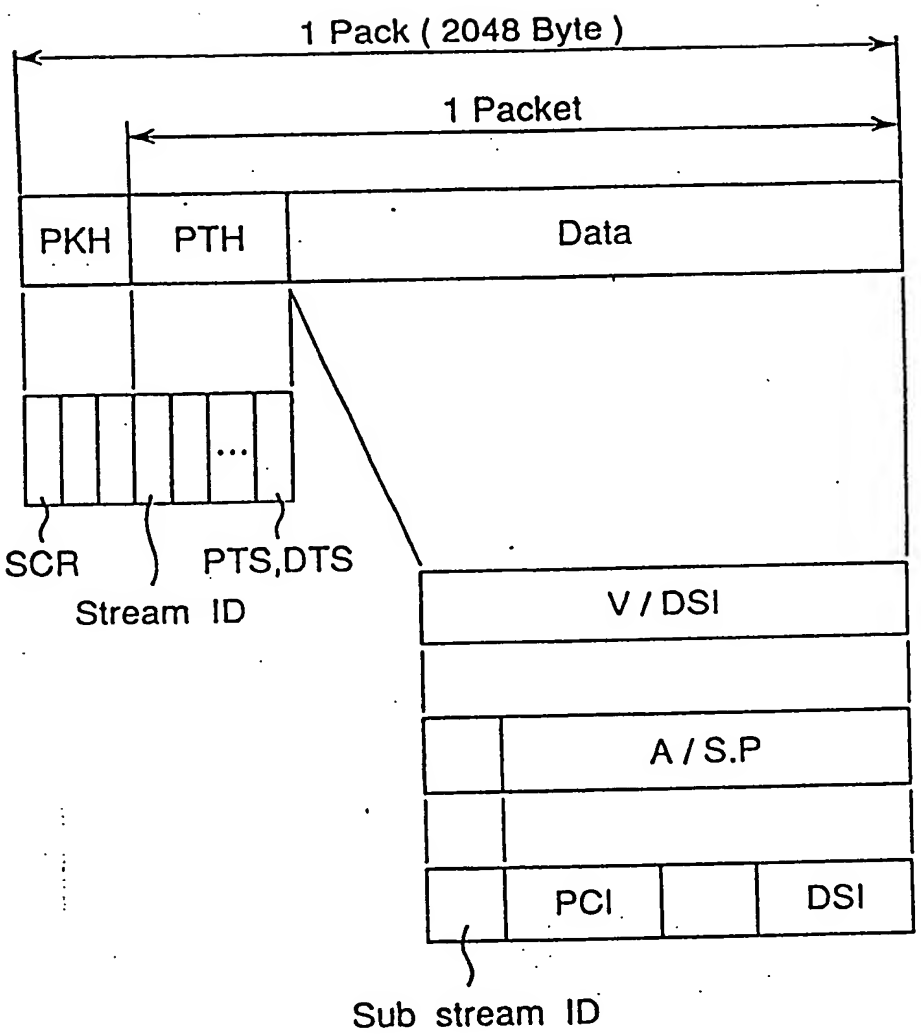
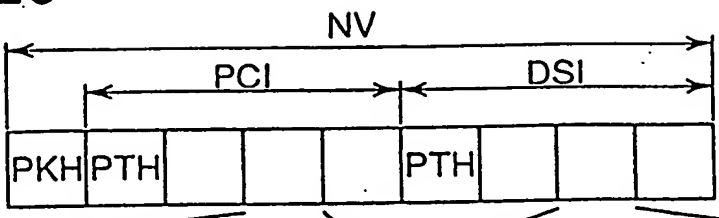

1 Pack (2048 Byte)
1 Packet
PKH
PTH
Data
SCR
PTS,DTS
Stream ID
V / DSI
A / S.P
PCI
DSI
Sub stream ID

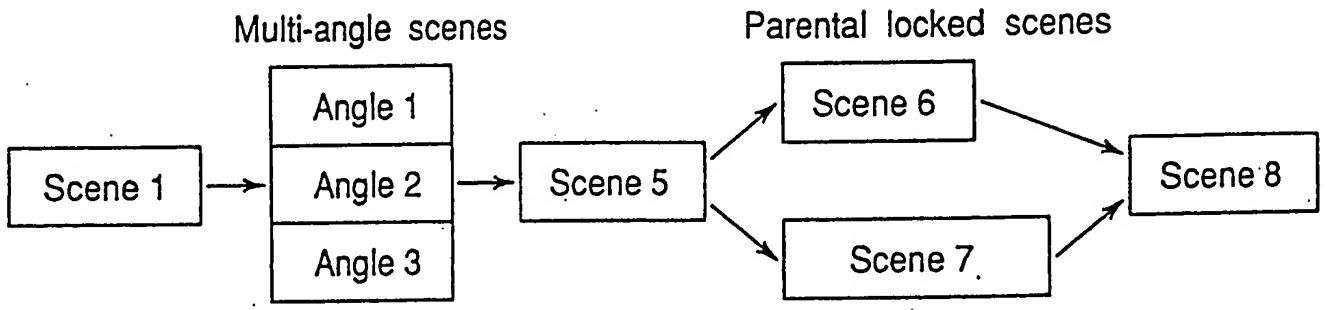
Fig.20



PCI general information (PCI_GI)
Start PTM of VOBU (VOBU_S_PTM) End PTM of VOBU (VOBU_E_PTM)
Angle information for non-seamless (NSML_AGLI)
Destination address of angle cell number 1 (NSML_AGL_C1_DSTA) ⋮ Destination address of angle cell number 9 (NSML_AGL_C9_DSTA)

DSI general information (DSI_GI)
End address for VOB (VOBU_EA)
Seamless playback information (SML_PBI)
Interleaved unit flag (ILVU flag) Unit end flag (Unit END flag) Interleaved unit end address (ILVU_EA) Next interleaved unit end address (NT_ILVU_SA) Audio stop PTM 1 in VOB (VOB_A_STP_PTM1) Audio gap length 1 in VOB (VOB_A_GAP_LEN1) Audio stop PTM 2 in VOB (VOB_A_STP_PTM2) Audio gap length 2 in VOB (VOB_A_GAP_LEN2)
Angle information for seamless (SML_AGLI)
Destination address of angle cell number 1 (SML_AGL_C1_DSTA) ⋮ Destination address of angle cell number 9 (SML_AGL_C9_DSTA)

Fig.21



Scenario 1

Scene 1 → Multi-angle scenes → Scene 5 → Scene 6 → Scene 8

Scenario 2

Scene 1 → Multi-angle scenes → Scene 5 → Scene 7 → Scene 8

Fig.22

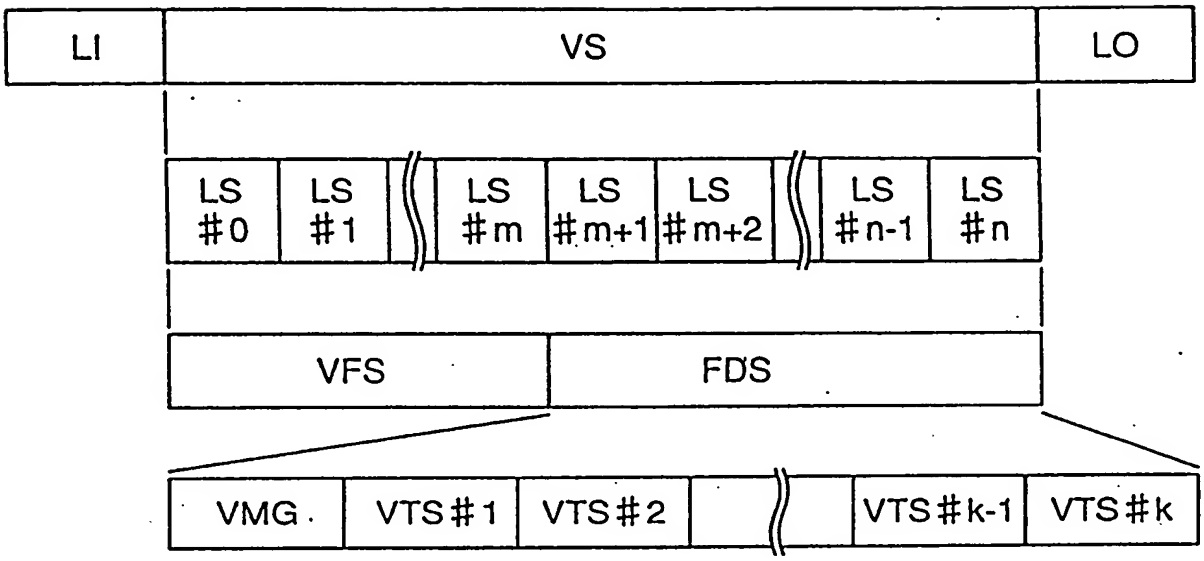

LI
VS
LO
LS #0
LS #1
LS #m
LS #m+1
LS #m+2
LS #n-1
LS #n
VFS
FDS
VMG
VTS#1
VTS#2
VTS#k-1
VTS#k

Fig.24

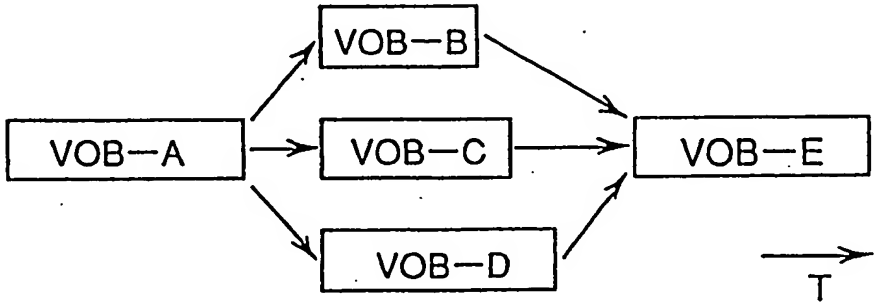

VOB—A
VOB—B
VOB—C
VOB—D
VOB—E
T

Fig.23

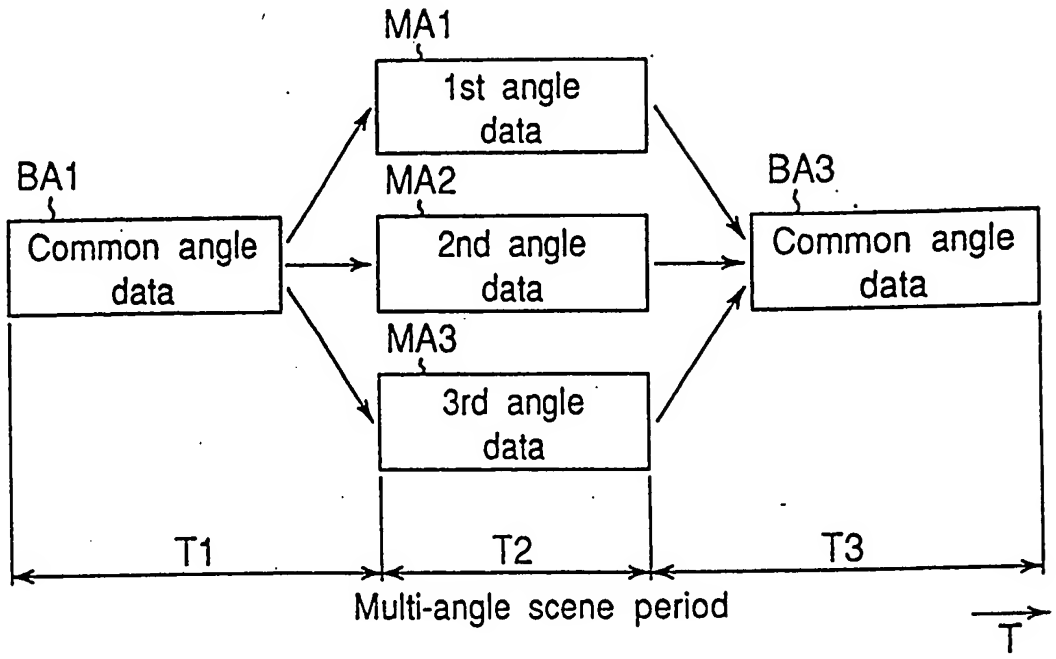

MA1
1st angle data
BA1
MA2
BA3
Common angle data
2nd angle data
Common angle data
MA3
3rd angle data
T1
T2
T3
Multi-angle scene period
T

Fig.25

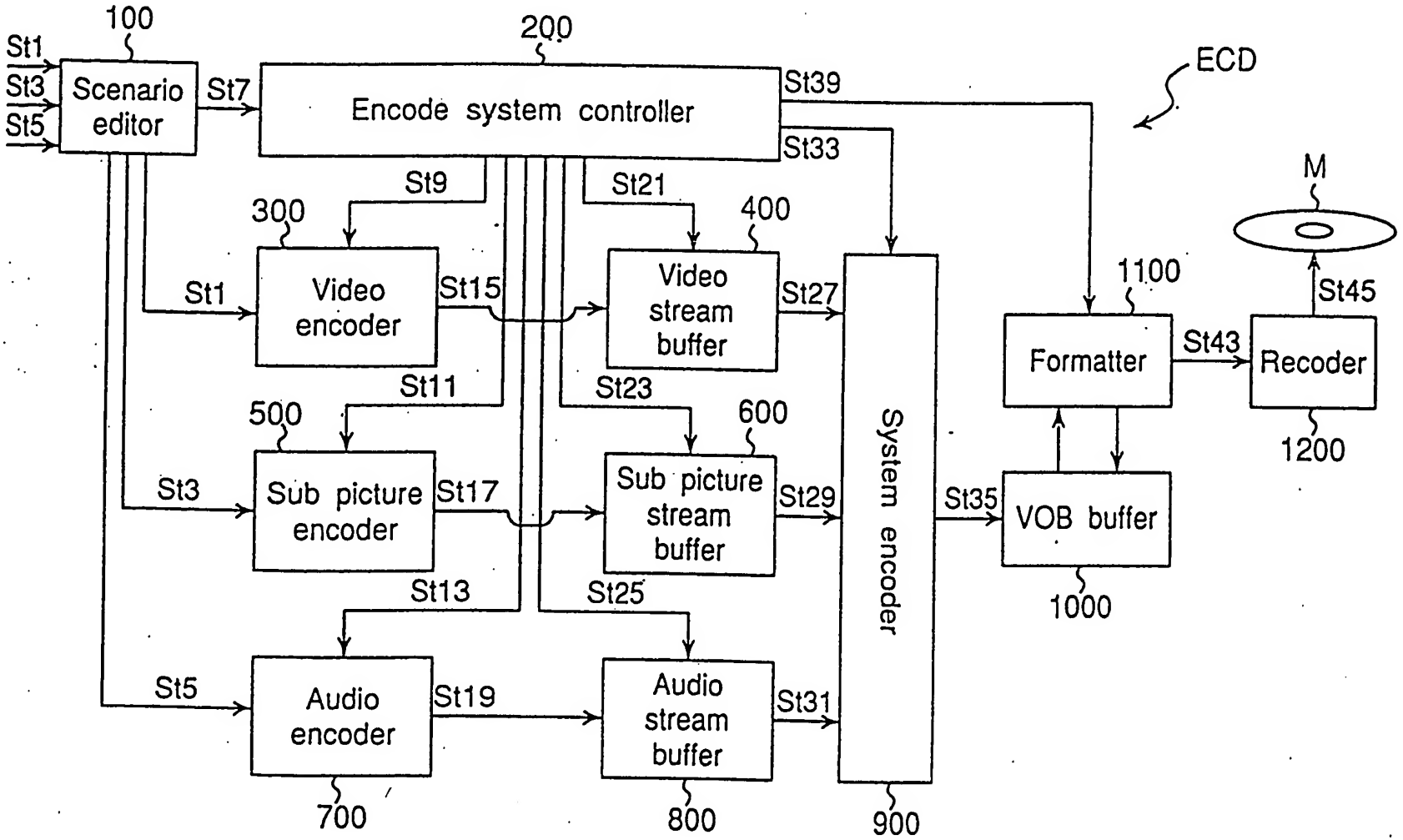

100
200
St1
St3
St5
Scenario editor
St7
Encode system controller
St39
St33
ECD
M
St9
St21
300
400
St1
Video encoder
St15
Video stream buffer
St27
1100
St45
Formatter
St43
Recoder
St11
St23
500
600
1200
St3
Sub picture encoder
St17
Sub picture stream buffer
St29
St35
VOB buffer
System encoder
St13
St25
1000
St5
Audio encoder
St19
Audio stream buffer
St31
700
800
900

Fig.26

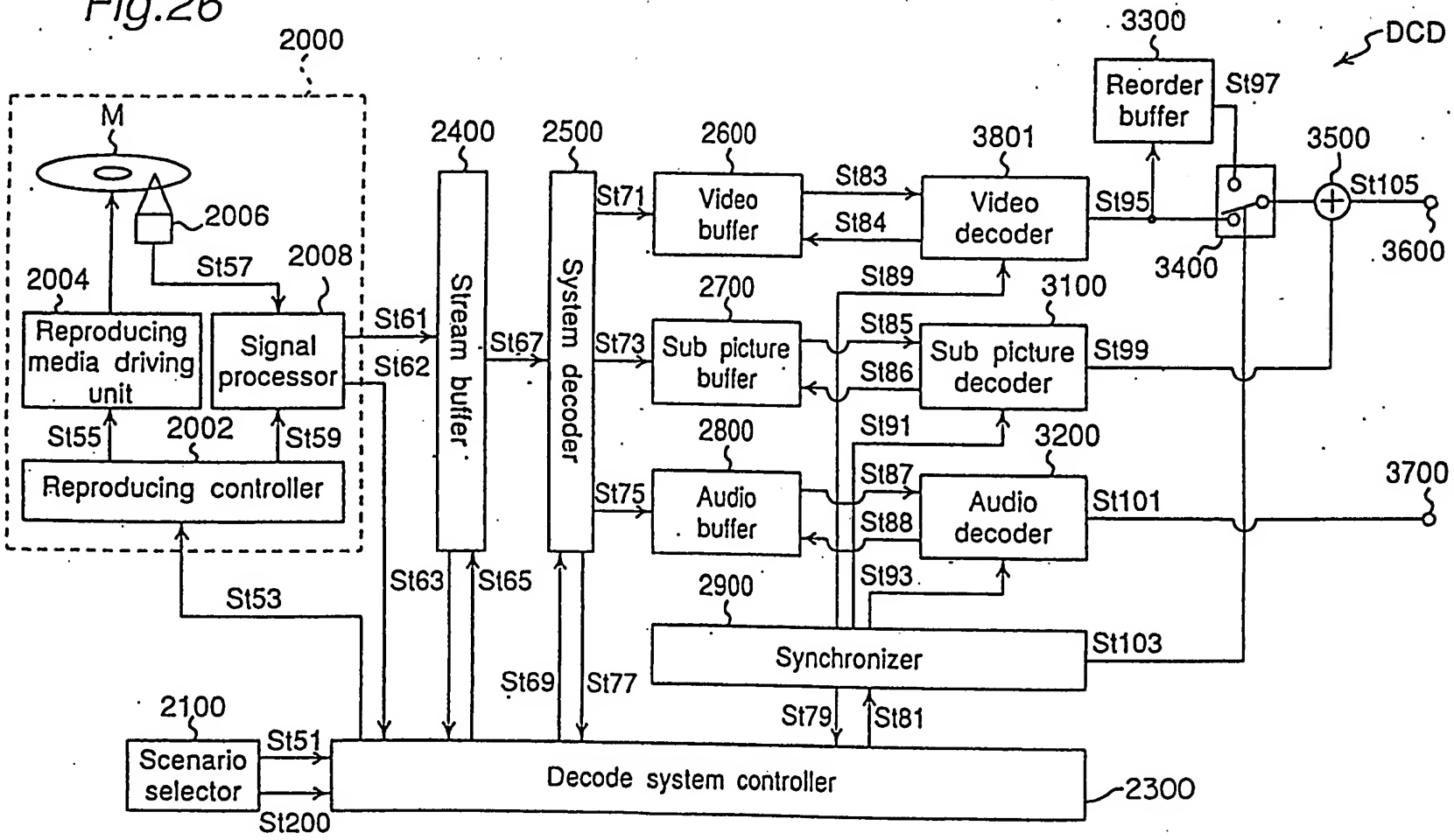

2000
M
2006
2004
St57
2008
Reproducing media driving unit
Signal processor
St61
St62
St55
2002
St59
Reproducing controller
St53
2100
Scenario selector
St51
St200
St63
St65
2400
Stream buffer
St67
2500
System decoder
St69
St77
St71
St73
St75
2600
Video buffer
2700
Sub picture buffer
2800
Audio buffer
2900
Synchronizer
St79
St81
St83
St84
St89
St85
St86
St91
St87
St88
St93
3801
Video decoder
3100
Sub picture decoder
3200
Audio decoder
3300
Reorder buffer
St97
St95
3400
3500
St105
3600
DCD
St99
St101
3700
St103
Decode system controller
2300

Fig.27

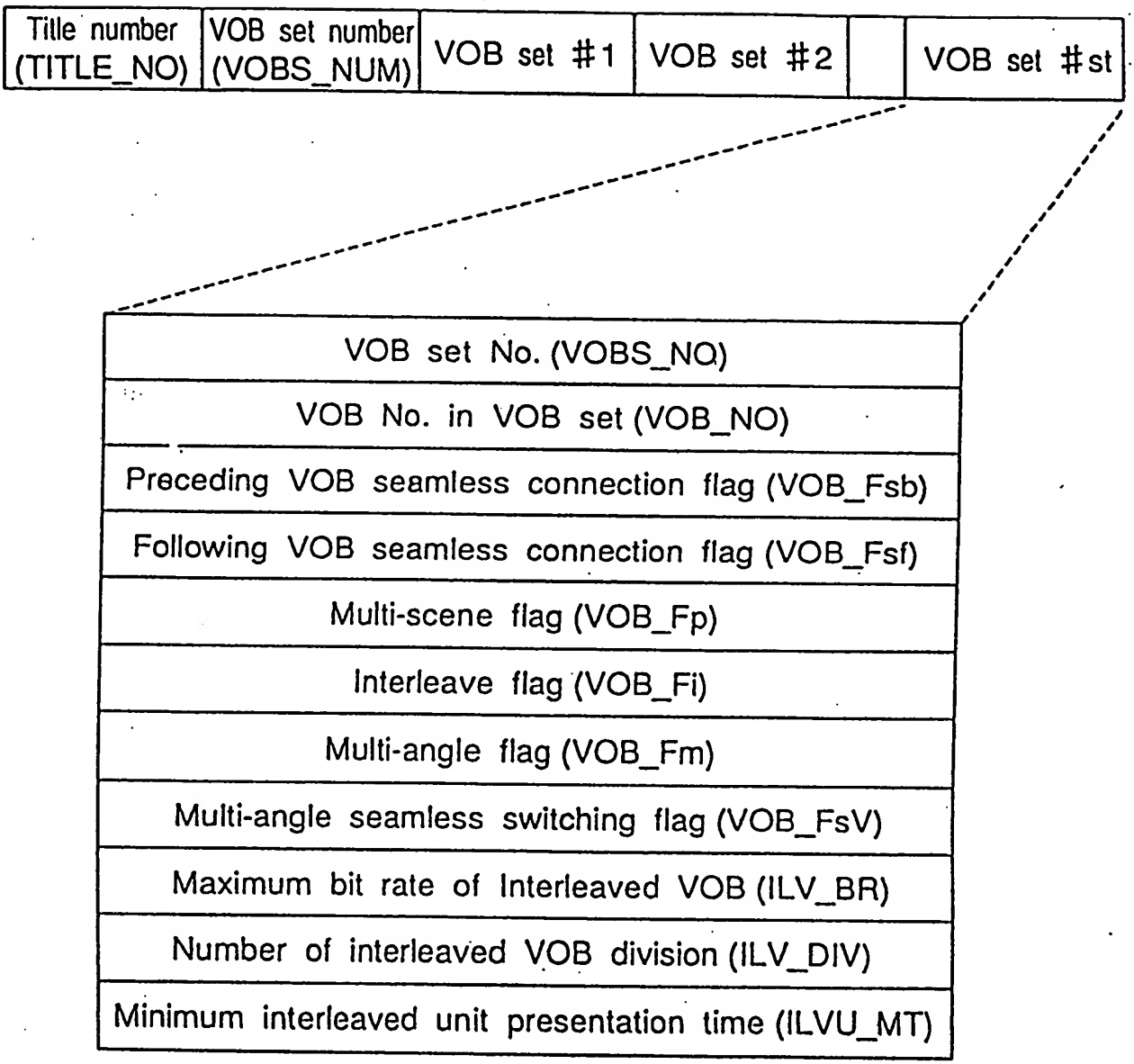

Title number (TITLE_NO)
VOB set number (VOBS_NUM)
VOB set #1
VOB set #2
VOB set #st
VOB set No. (VOBS_NO)
VOB No. in VOB set (VOB_NO)
Preceding VOB seamless connection flag (VOB_Fsb)
Following VOB seamless connection flag (VOB_Fsf)
Multi-scene flag (VOB_Fp)
Interleave flag (VOB_Fi)
Multi-angle flag (VOB_Fm)
Multi-angle seamless switching flag (VOB_FsV)
Maximum bit rate of Interleaved VOB (ILV_BR)
Number of interleaved VOB division (ILV_DIV)
Minimum interleaved unit presentation time (ILVU_MT)

Fig.28

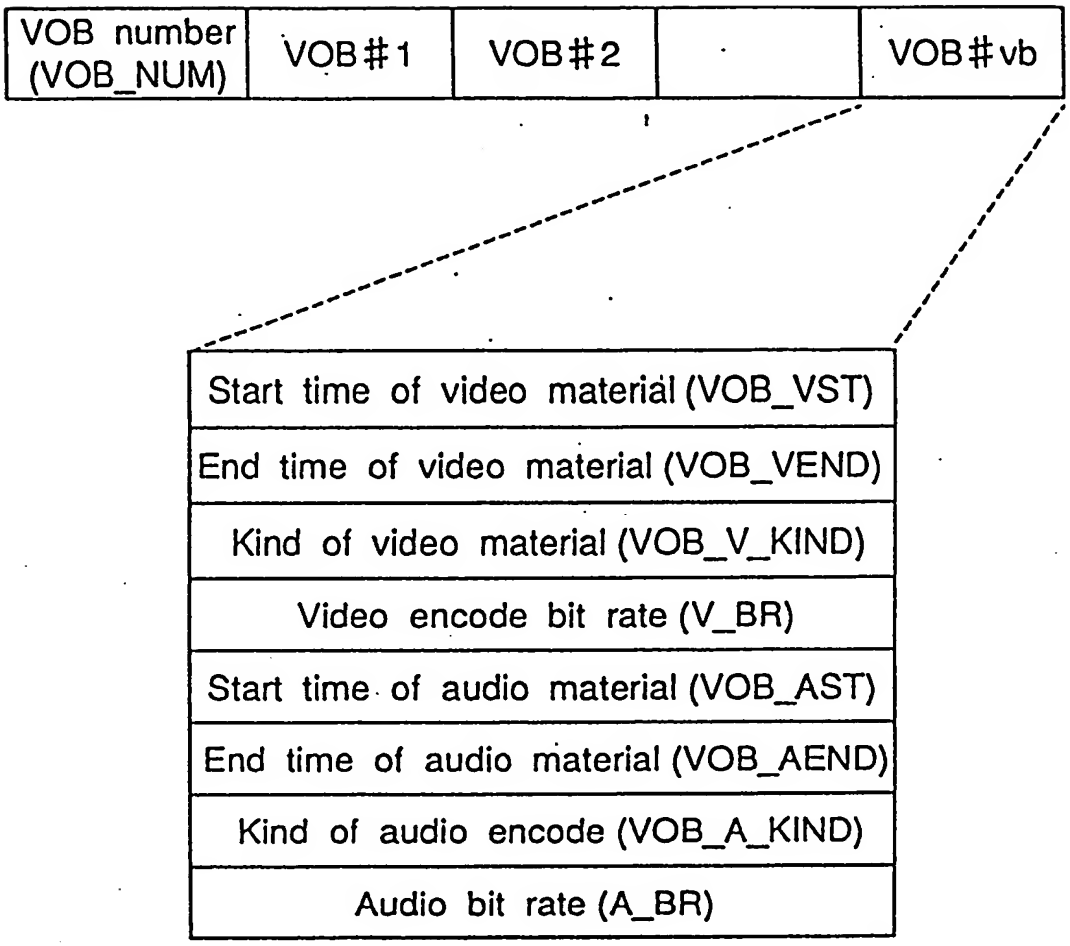

VOB number (VOB_NUM)
VOB#1
VOB#2
VOB#vb
Start time of video material (VOB_VST)
End time of video material (VOB_VEND)
Kind of video material (VOB_V_KIND)
Video encode bit rate (V_BR)
Start time of audio material (VOB_AST)
End time of audio material (VOB_AEND)
Kind of audio encode (VOB_A_KIND)
Audio bit rate (A_BR)

Fig.29

VOB number (VOB_NO)
Video encode start time (V_STTM)
Video encode end time (V_ENDTM)
Video encode mode (V_ENCMD)
Video encode bit rate (V_RATE)
Video encode maximum bit rate (V_MRATE)
GOP structure fixing flag (GOP_FXflag)
Video encode GOP structure (GOPST)
Video encode initial data (V_INST)
Video encode end data (V_ENDST)
Audio encode start time (A_STTM)
Audio encode end time (A_ENDTM)
Audio encode bit rate (A_RATE)
AUdio encode method (A_ENCMD)
Audio start gap (A_STGAP)
Audio end gap (A_ENDGAP)
Preceding VOB number (B_VOB_NO)
Following VOB number (F_VOB_NO)

Fig.30

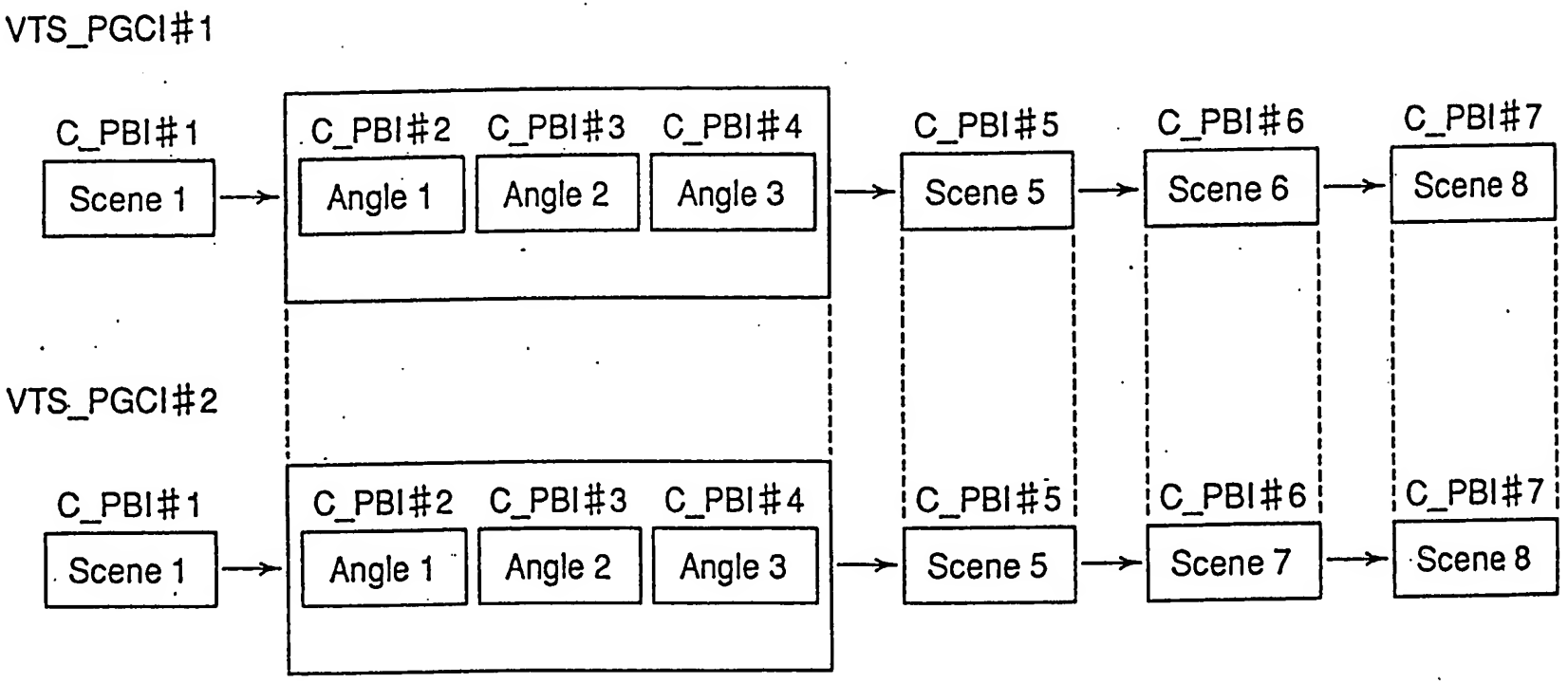

VTS_PGCI#1
C_PBI#1
Scene 1
C_PBI#2
C_PBI#3
C_PBI#4
Angle 1
Angle 2
Angle 3
C_PBI#5
Scene 5
C_PBI#6
Scene 6
C_PBI#7
Scene 8
VTS_PGCI#2
C_PBI#1
Scene 1
C_PBI#2
C_PBI#3
C_PBI#4
Angle 1
Angle 2
Angle 3
C_PBI#5
Scene 5
C_PBI#6
Scene 7
C_PBI#7
Scene 8

Fig.31

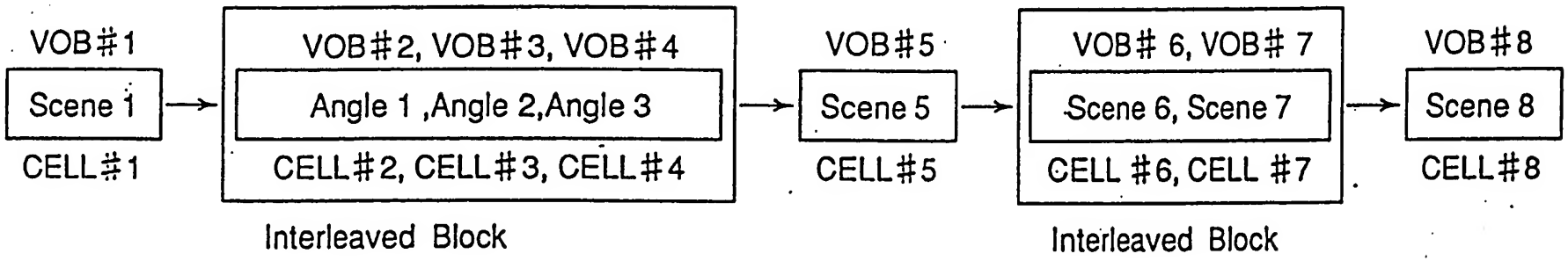

VOB#1
Scene 1
CELL#1
VOB#2, VOB#3, VOB#4
Angle 1 ,Angle 2,Angle 3
CELL#2, CELL#3, CELL#4
Interleaved Block
VOB#5
Scene 5
CELL#5
VOB# 6, VOB# 7
Scene 6, Scene 7
CELL #6, CELL #7
Interleaved Block
VOB#8
Scene 8
CELL#8

Fig.32

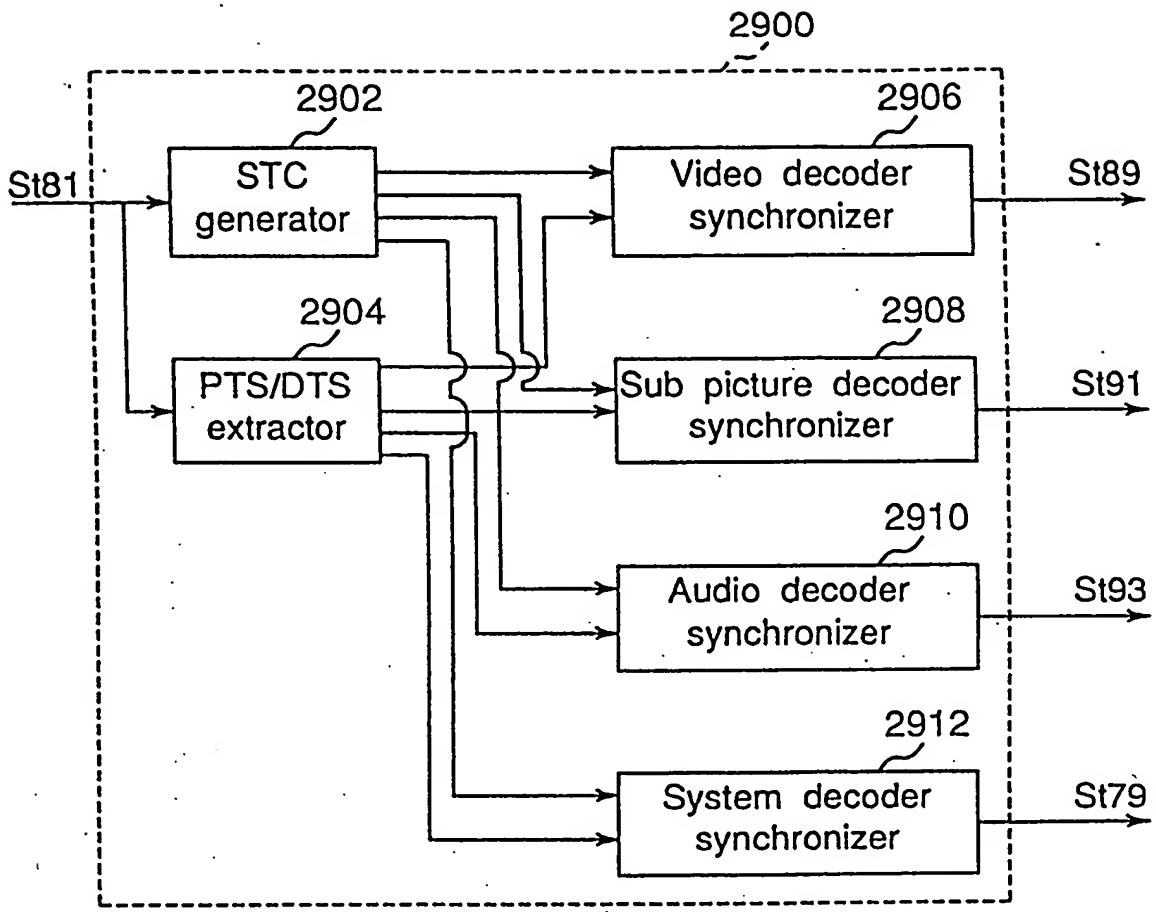

2900
2902
2906
St81
STC generator
Video decoder synchronizer
St89
2904
2908
PTS/DTS extractor
Sub picture decoder synchronizer
St91
2910
Audio decoder synchronizer
St93
2912
System decoder synchronizer
St79

Fig.33

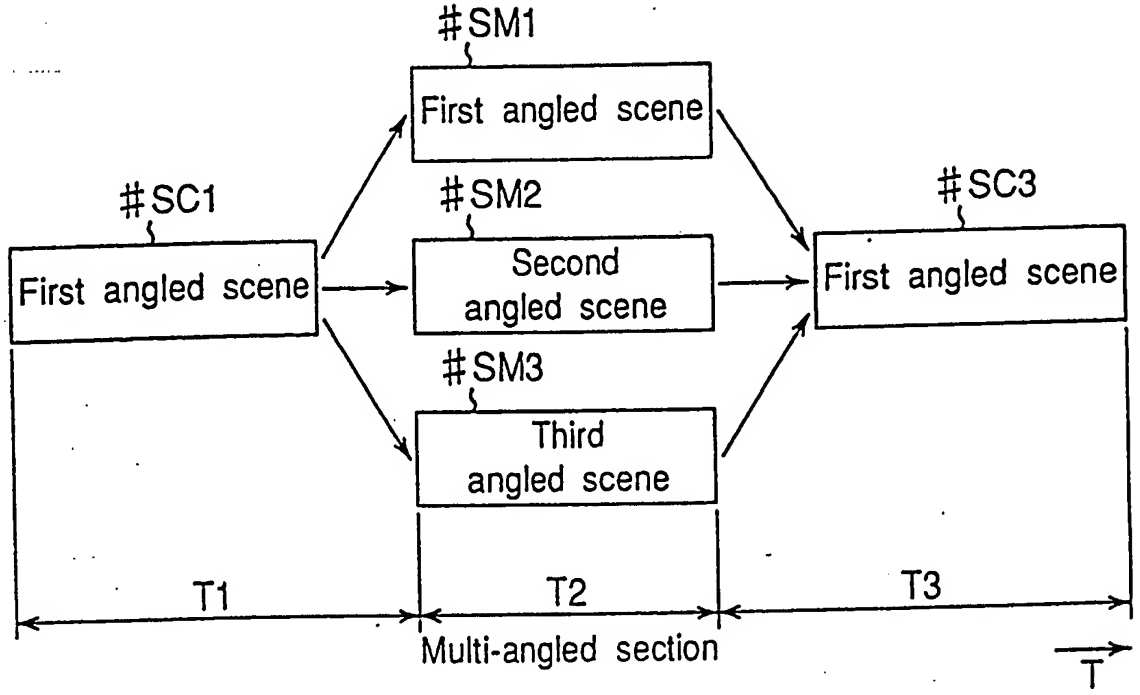

#SM1
First angled scene
#SC1
#SM2
#SC3
First angled scene
Second angled scene
First angled scene
#SM3
Third angled scene
T1
T2
T3
Multi-angled section
T

Fig.34

Fig.34A

Fig.34B

Fig. 34A

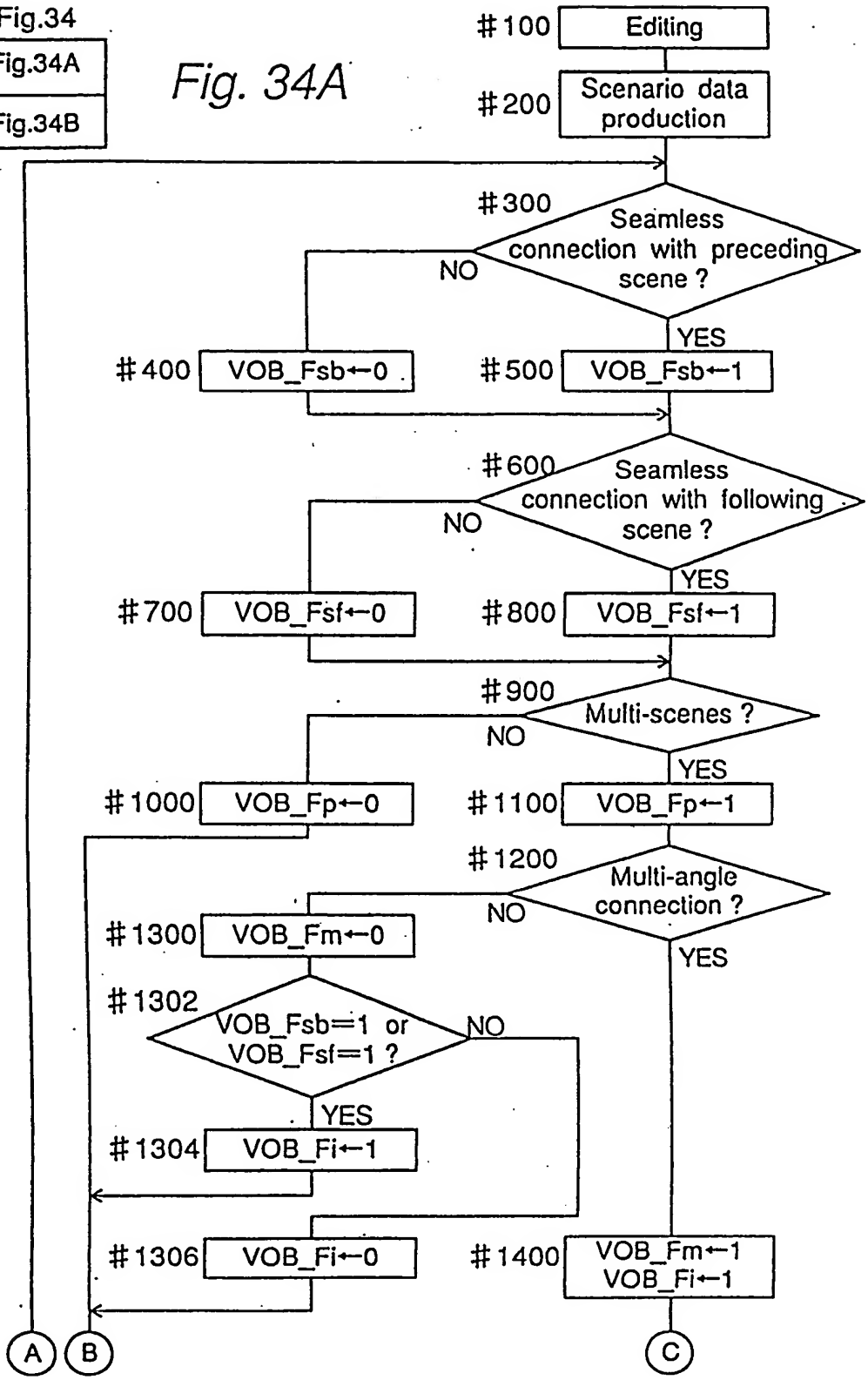


Fig.34B

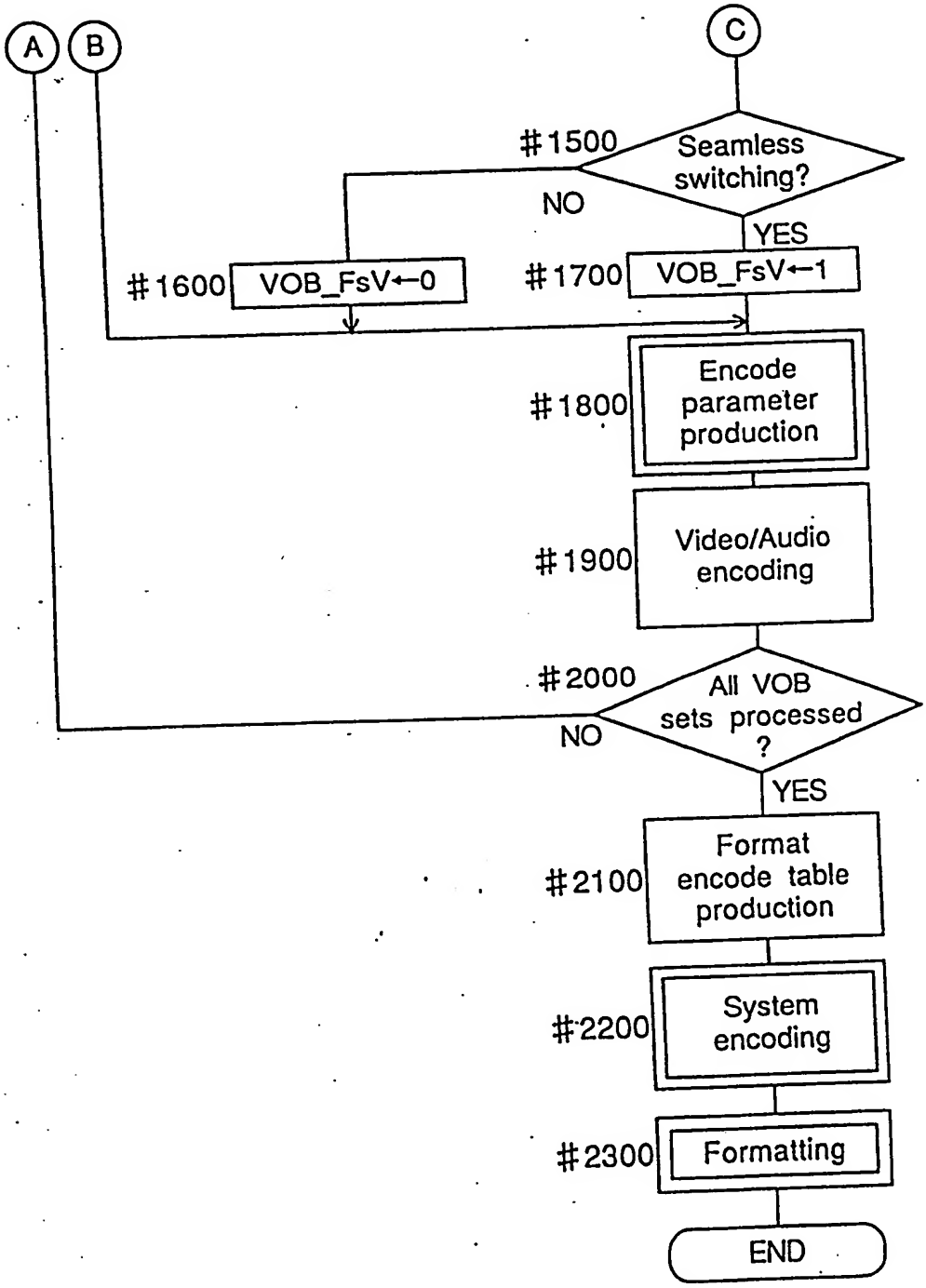

A
B
C
#1500
Seamless switching?
NO
YES
#1600
VOB_FsV←0
#1700
VOB_FsV←1
#1800
Encode parameter production
#1900
Video/Audio encoding
#2000
All VOB sets processed ?
NO
YES
#2100
Format encode table production
#2200
System encoding
#2300
Formatting
END

Fig.35

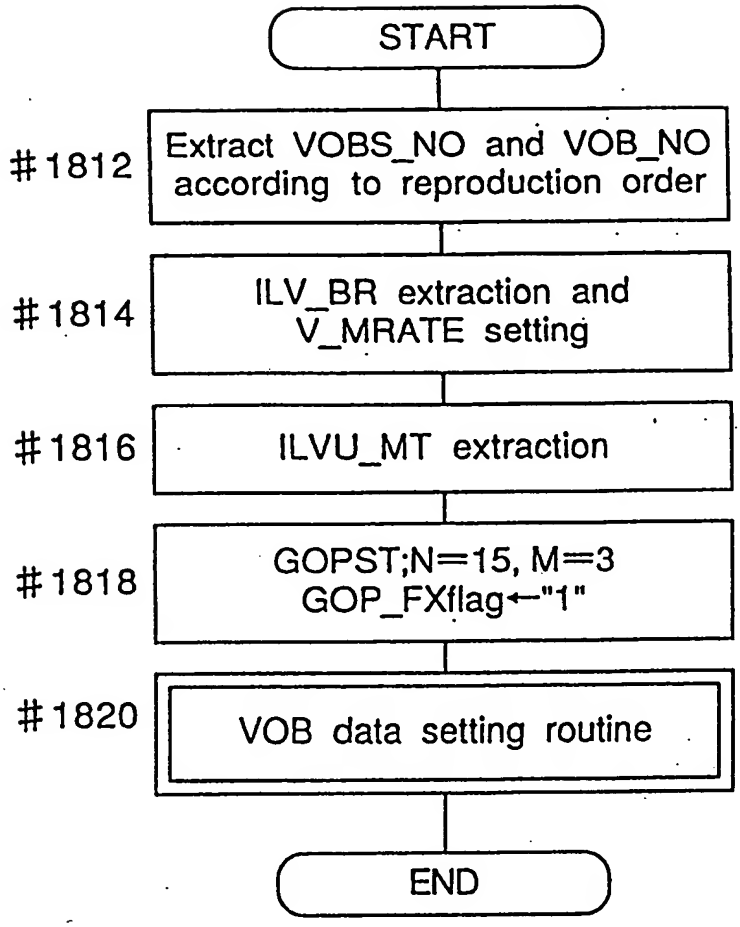

START
#1812
Extract VOBS_NO and VOB_NO according to reproduction order
#1814
ILV_BR extraction and V_MRATE setting
#1816
ILVU_MT extraction
#1818
GOPST;N=15, M=3
GOP_FXflag←"1"
#1820
VOB data setting routine
END

Fig.36

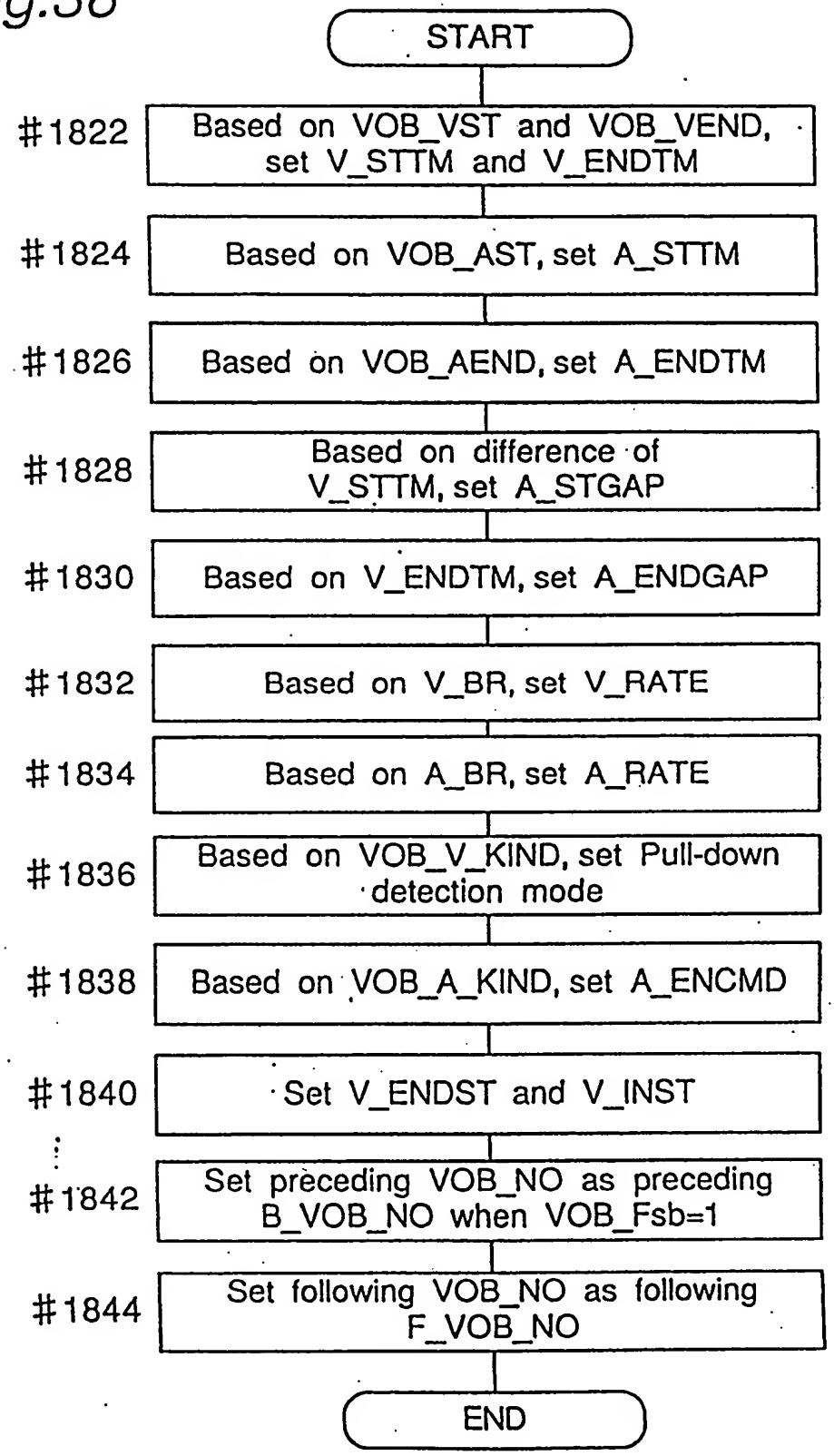

START
#1822
Based on VOB_VST and VOB_VEND, set V_STTM and V_ENDTM
#1824
Based on VOB_AST, set A_STTM
#1826
Based on VOB_AEND, set A_ENDTM
#1828
Based on difference of V_STTM, set A_STGAP
#1830
Based on V_ENDTM, set A_ENDGAP
#1832
Based on V_BR, set V_RATE
#1834
Based on A_BR, set A_RATE
#1836
Based on VOB_V_KIND, set Pull-down detection mode
#1838
Based on VOB_A_KIND, set A_ENCMD
#1840
Set V_ENDST and V_INST
#1842
Set preceding VOB_NO as preceding B_VOB_NO when VOB_Fsb=1
#1844
Set following VOB_NO as following F_VOB_NO
END

Fig.37

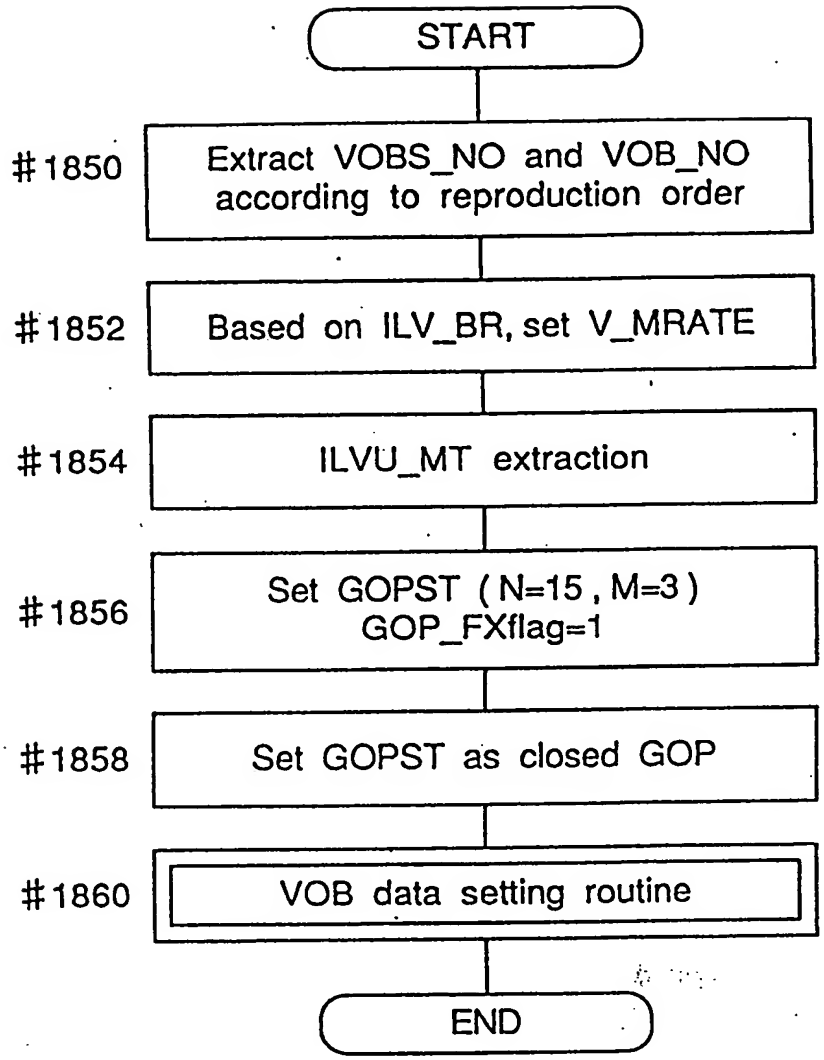

START
#1850
Extract VOBS_NO and VOB_NO according to reproduction order
#1852
Based on ILV_BR, set V_MRATE
#1854
ILVU_MT extraction
#1856
Set GOPST (N=15 , M=3) GOP_FXflag=1
#1858
Set GOPST as closed GOP
#1860
VOB data setting routine
END

Fig.38

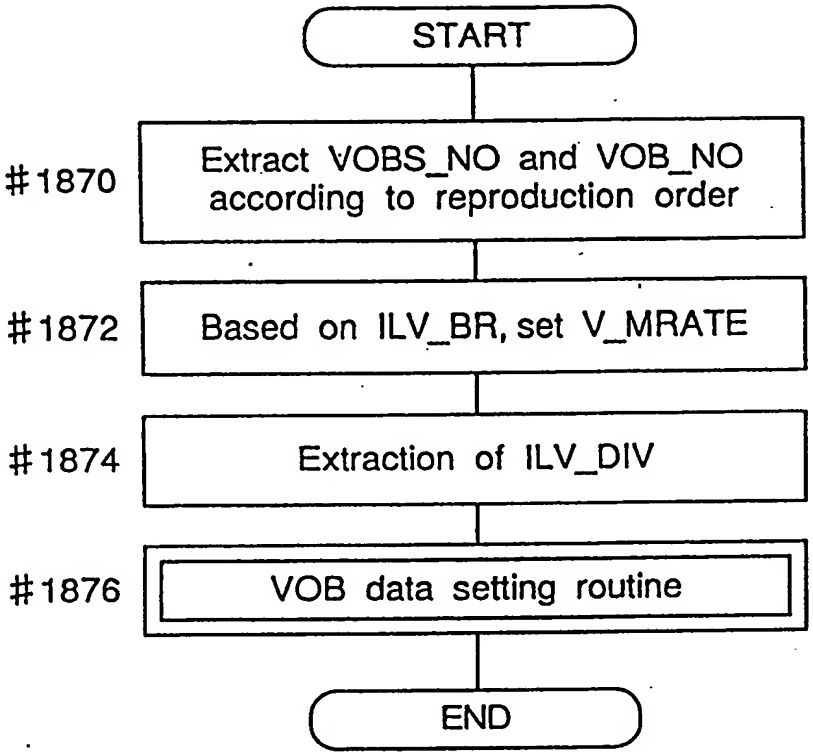

START
#1870
Extract VOBS_NO and VOB_NO according to reproduction order
#1872
Based on ILV_BR, set V_MRATE
#1874
Extraction of ILV_DIV
#1876
VOB data setting routine
END

Fig.39

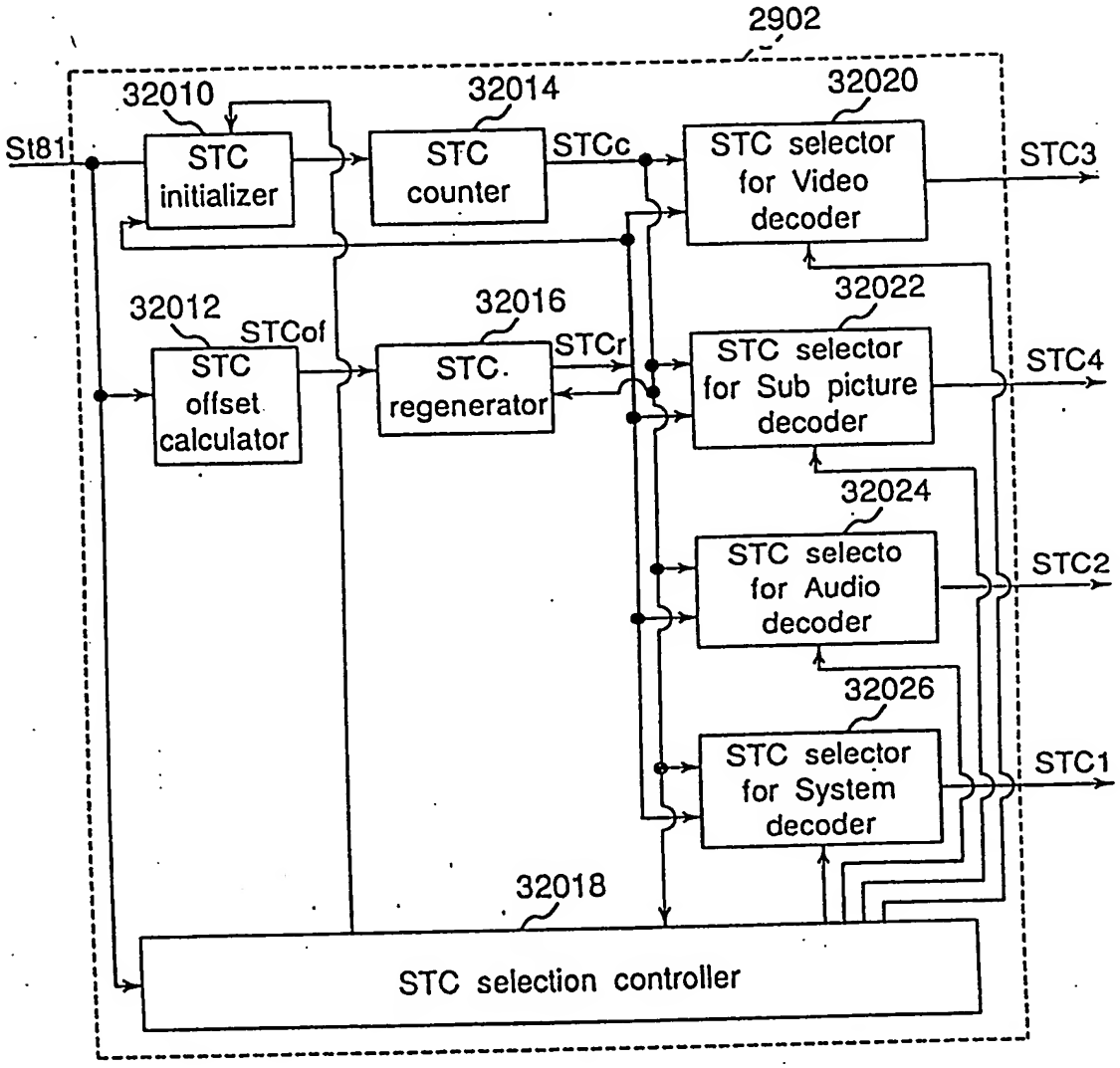

2902
32010
STC initializer
32014
STC counter
STCc
32020
STC selector for Video decoder
STC3
St81
32012
STCof
STC offset calculator
32016
STC regenerator
STCr
32022
STC selector for Sub picture decoder
STC4
32024
STC selecto for Audio decoder
STC2
32026
STC selector for System decoder
STC1
32018
STC selection controller

Fig.40A

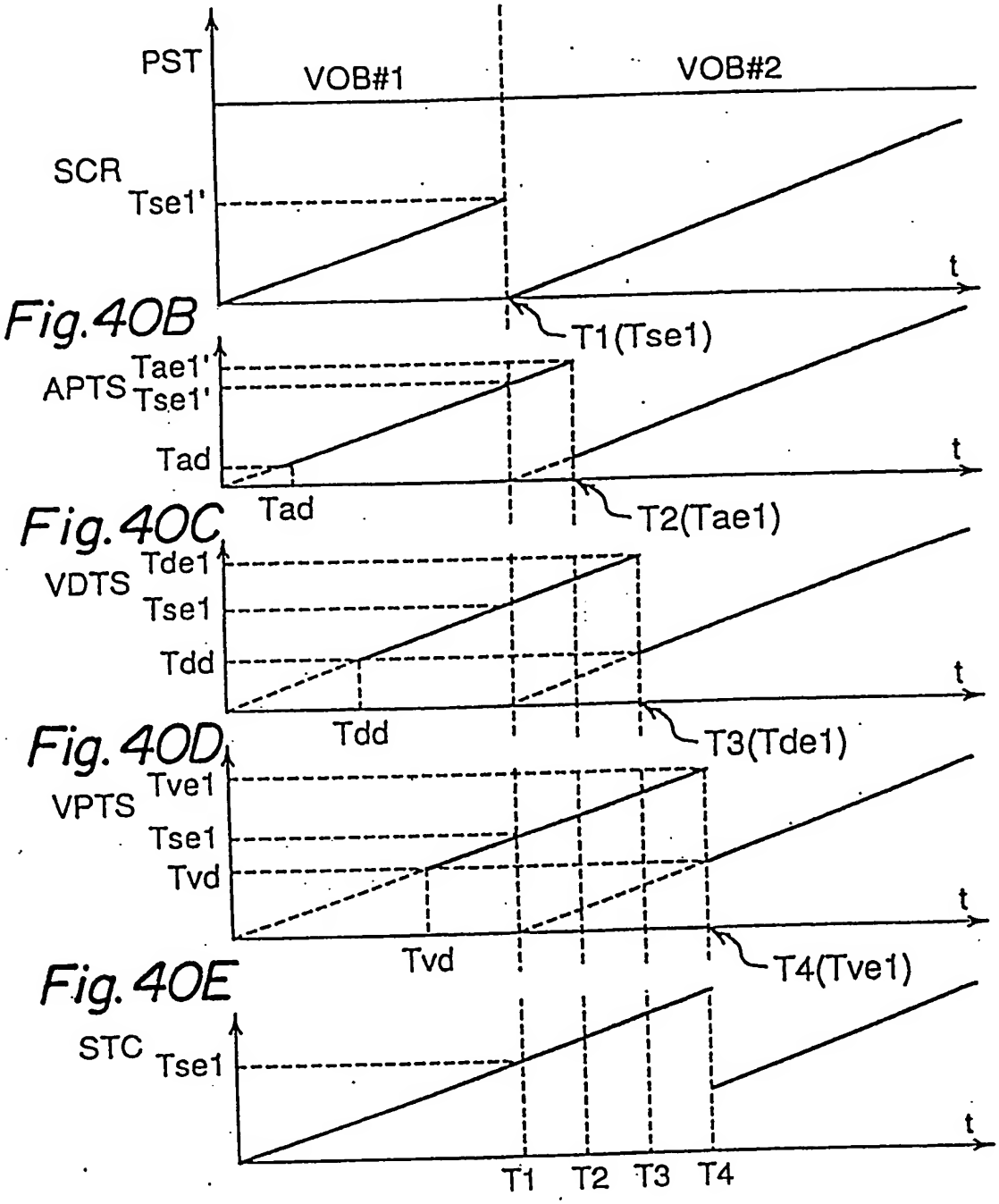


Fig.40B

Fig.40C

Fig.40D

Fig.40E

Fig.41

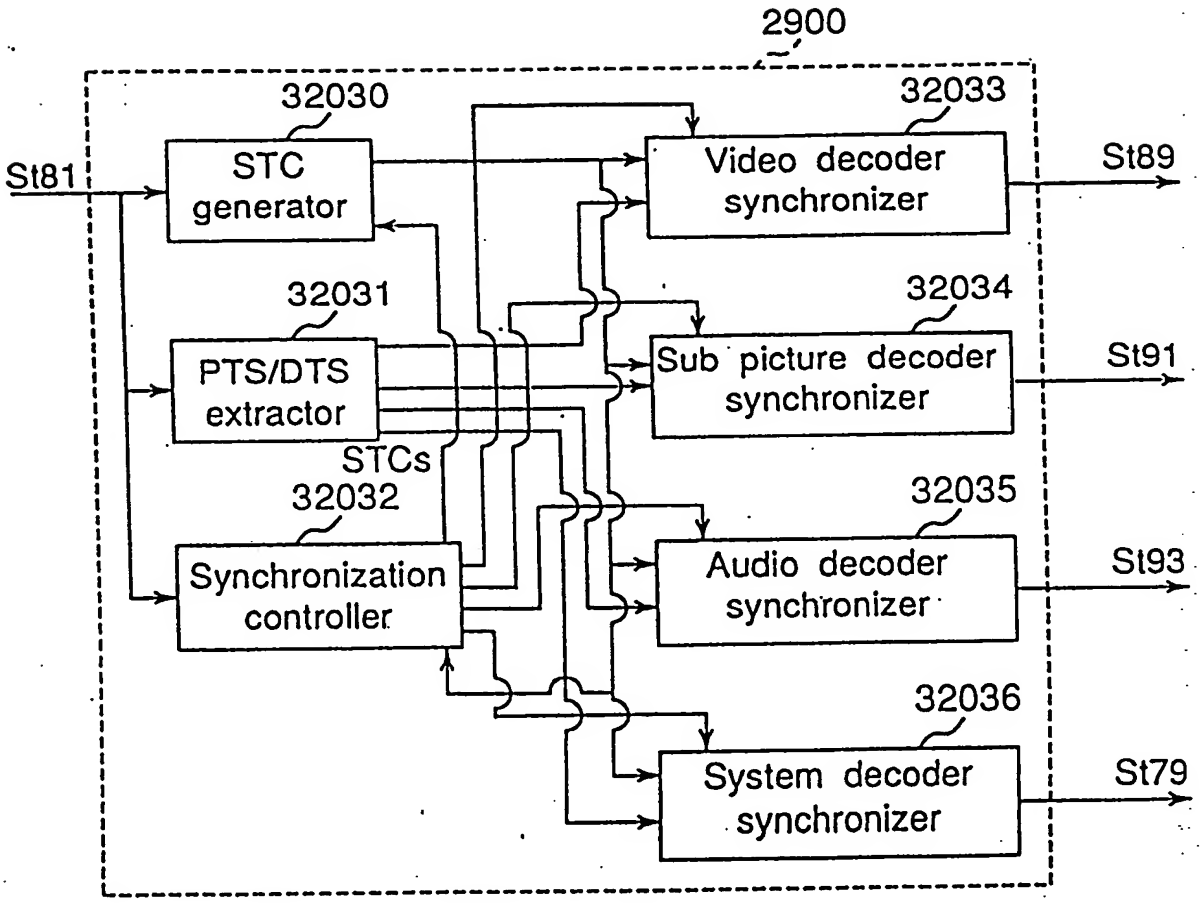

2900
32030
STC generator
St81
32031
PTS/DTS extractor
STCs
32032
Synchronization controller
32033
Video decoder synchronizer
St89
32034
Sub picture decoder synchronizer
St91
32035
Audio decoder synchronizer
St93
32036
System decoder synchronizer
St79

Fig.42

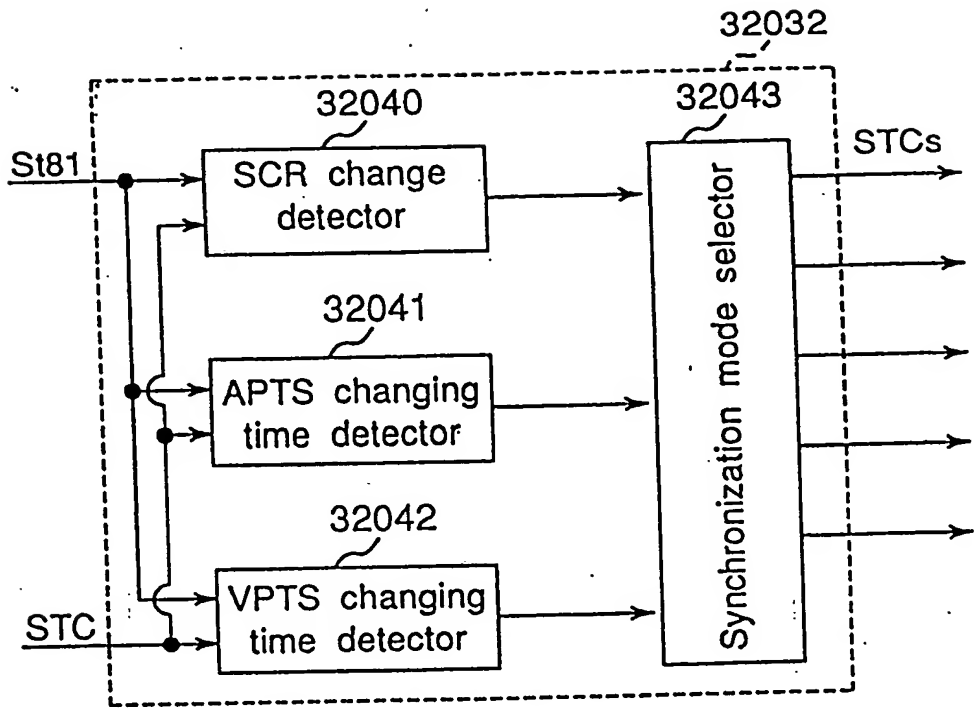

32032
32040
32043
St81
SCR change detector
STCs
32041
APTS changing time detector
Synchronization mode selector
32042
VPTS changing time detector
STC

Fig.43

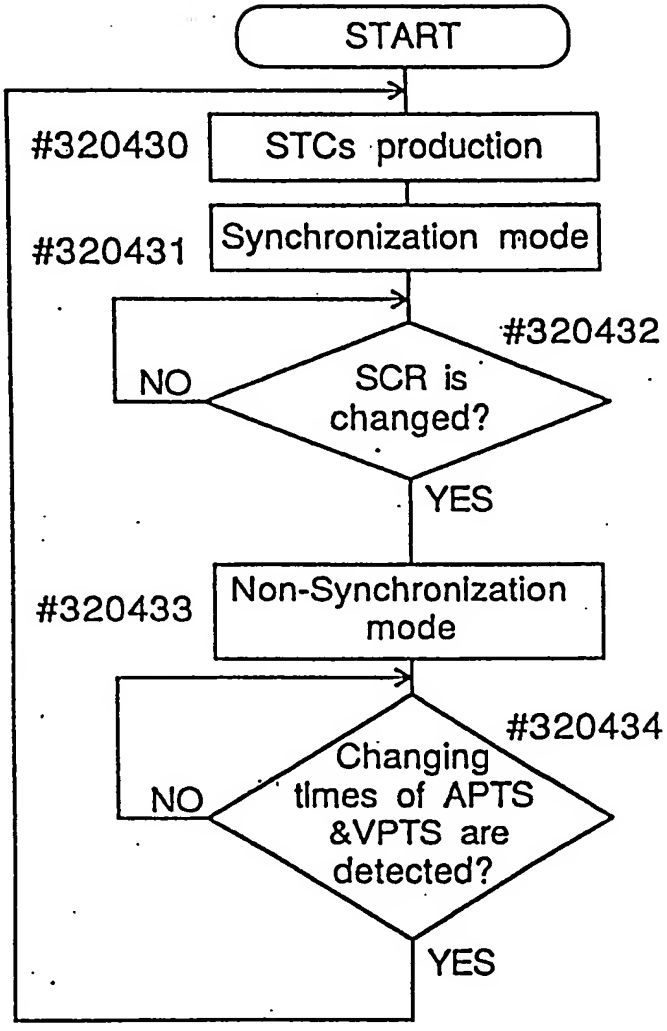

START
#320430
STCs production
#320431
Synchronization mode
#320432
NO
SCR is changed?
YES
#320433
Non-Synchronization mode
#320434
NO
Changing times of APTS &VPTS are detected?
YES

Fig.44A

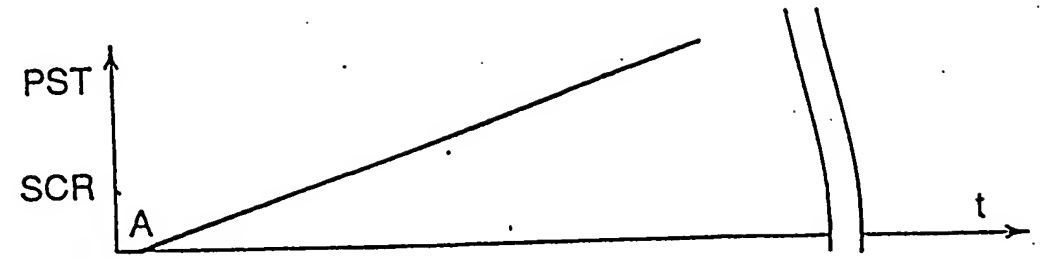


Fig.44B

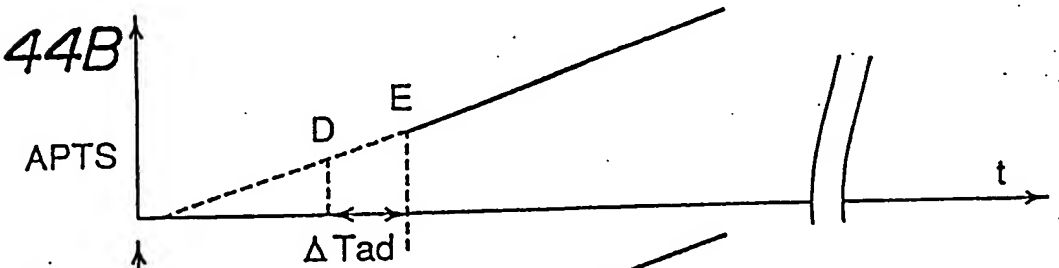


Fig.44C

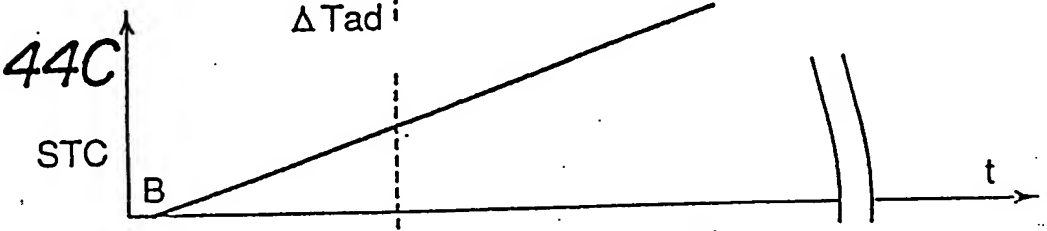


Fig.44D

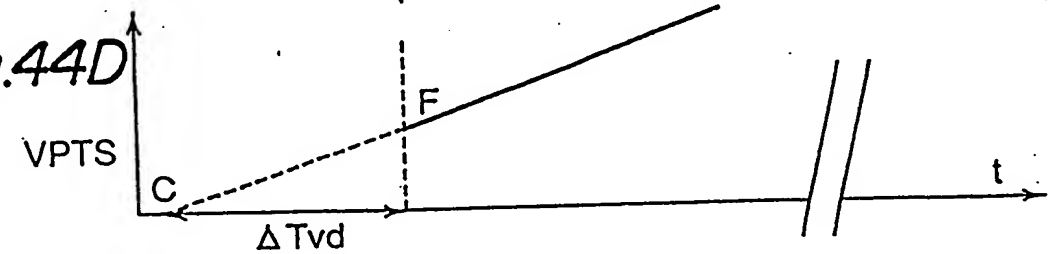


Fig.45A

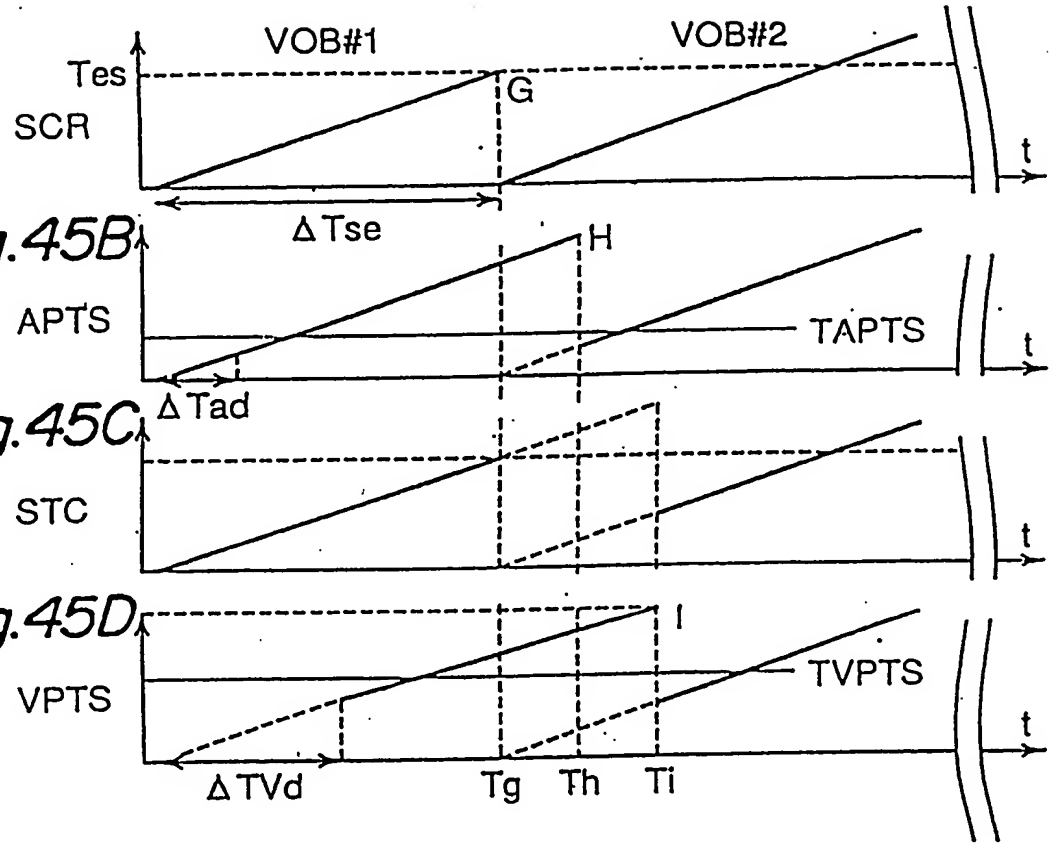


Fig.45B

Fig.45C

Fig.45D

Fig.46A

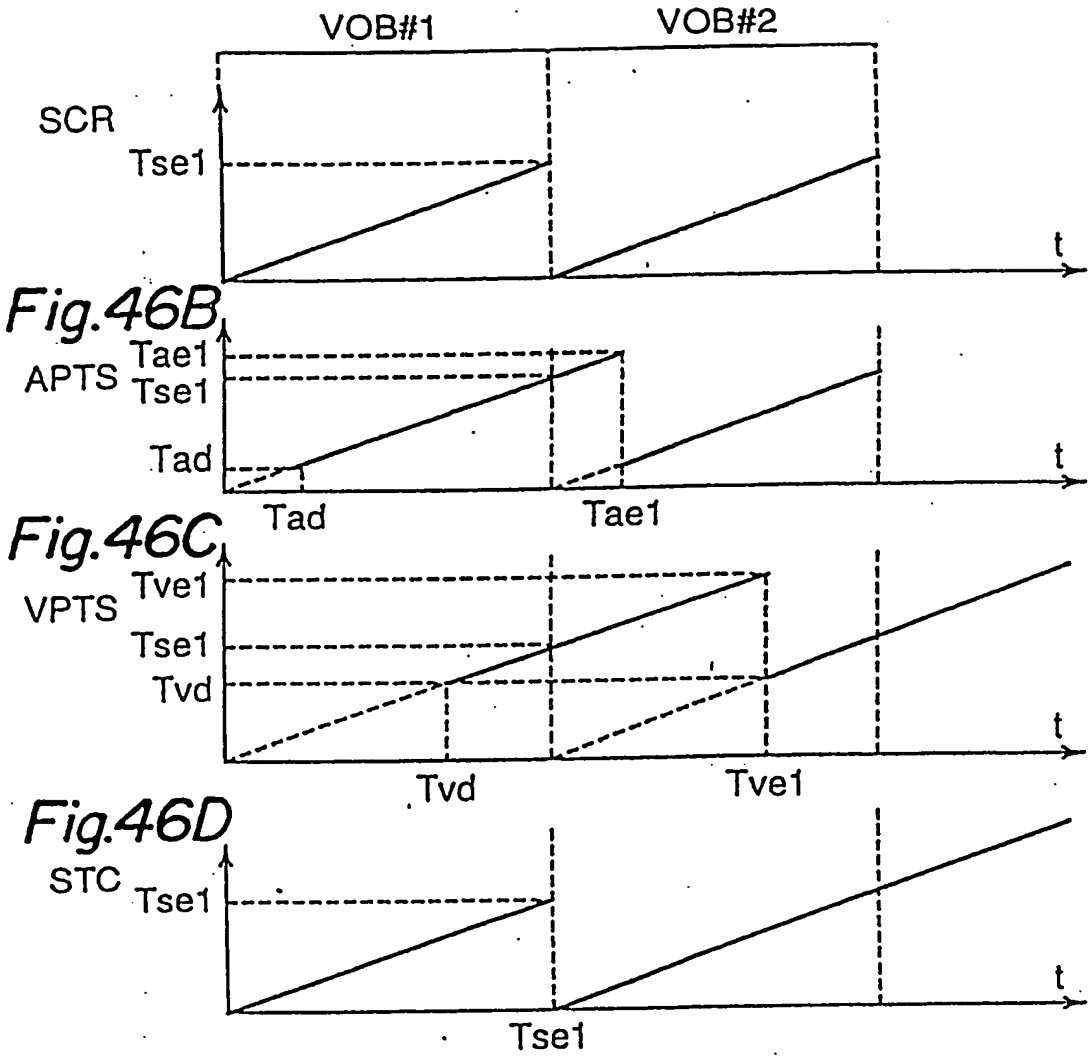


Fig.46B

Fig.46C

Fig.46D

Fig.47A

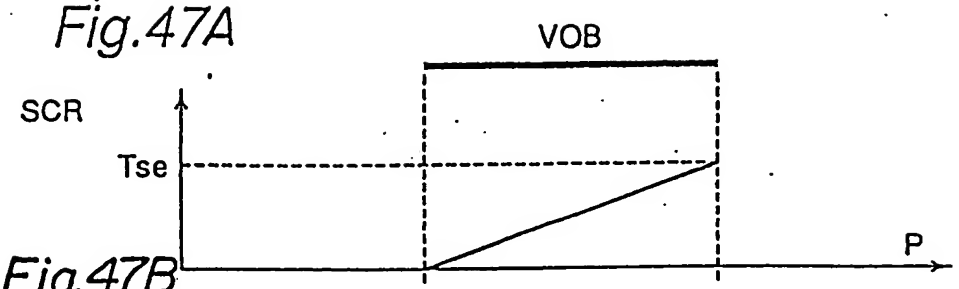


Fig.47B

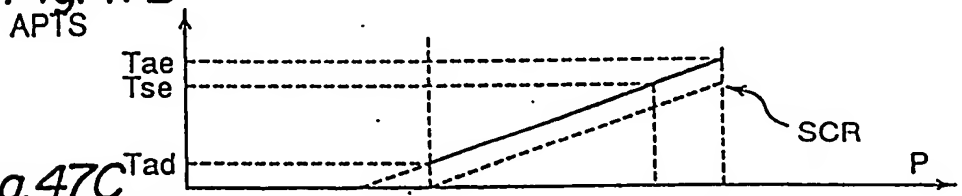


Fig.47C

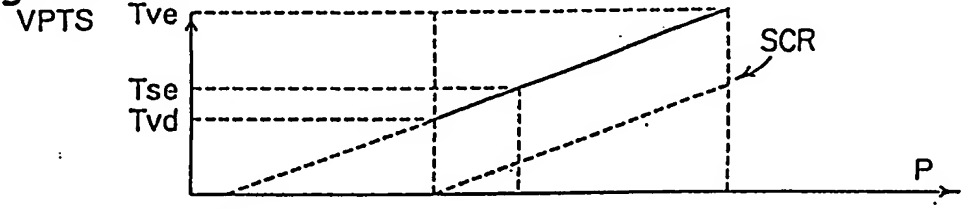


Fig.48A

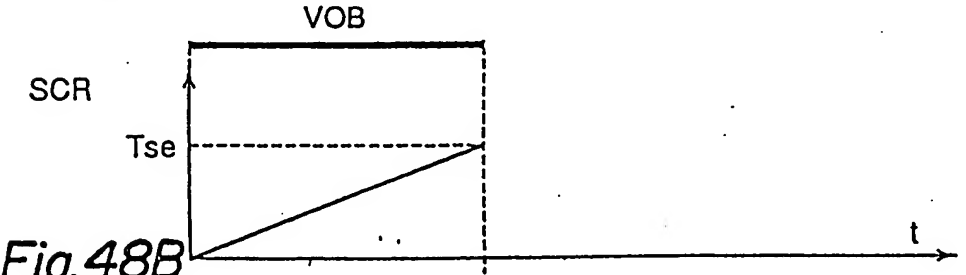


Fig.48B

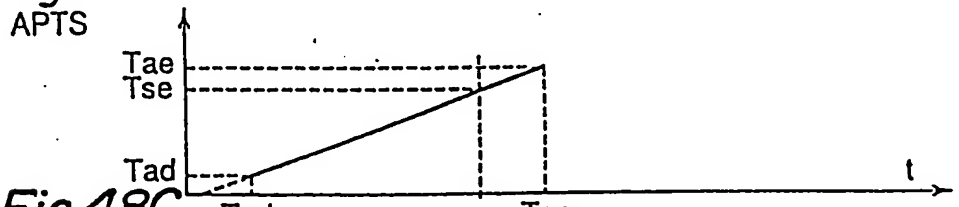


Fig.48C

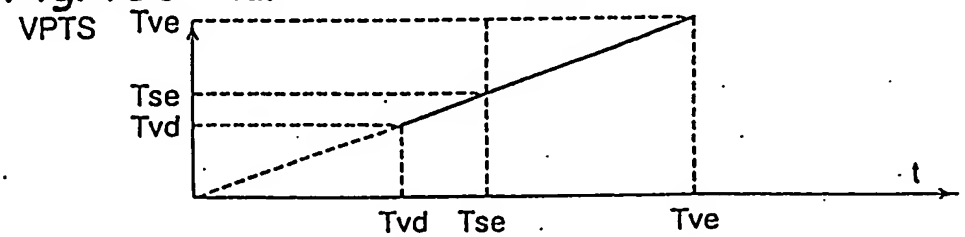


Fig.49

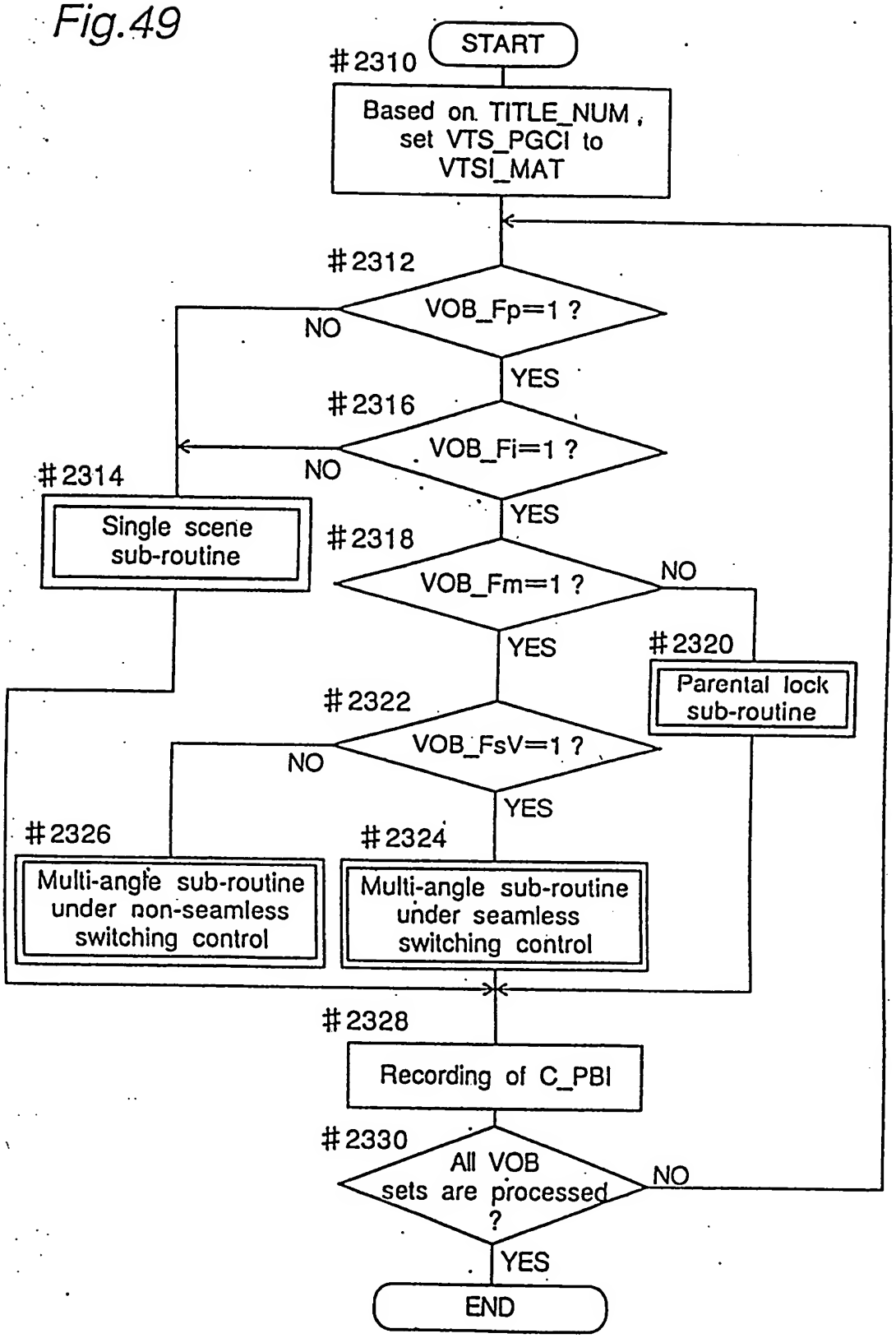

START
#2310
Based on TITLE_NUM, set VTS_PGCI to VTSI_MAT
#2312
VOB_Fp=1 ?
NO
YES
#2316
VOB_Fi=1 ?
NO
YES
#2314
Single scene sub-routine
#2318
VOB_Fm=1 ?
NO
YES
#2320
Parental lock sub-routine
#2322
VOB_FsV=1 ?
NO
YES
#2326
Multi-angle sub-routine under non-seamless switching control
#2324
Multi-angle sub-routine under seamless switching control
#2328
Recording of C_PBI
#2330
All VOB sets are processed ?
NO
YES
END

Fig.50

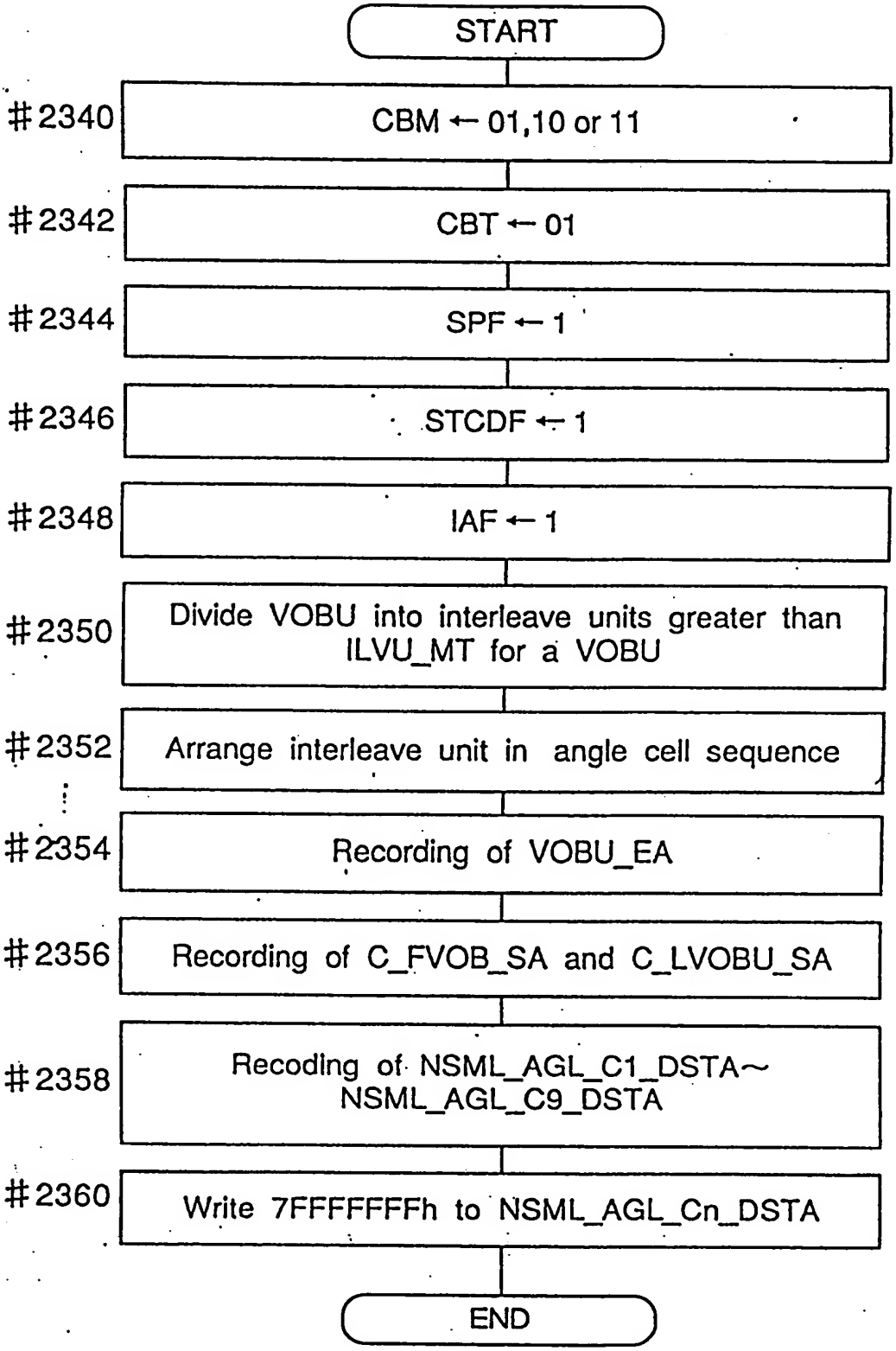

START
#2340
CBM ← 01,10 or 11
#2342
CBT ← 01
#2344
SPF ← 1
#2346
STCDF ← 1
#2348
IAF ← 1
#2350
Divide VOBU into interleave units greater than ILVU_MT for a VOBU
#2352
Arrange interleave unit in angle cell sequence
#2354
Recording of VOBU_EA
#2356
Recording of C_FVOB_SA and C_LVOBU_SA
#2358
Recoding of NSML_AGL_C1_DSTA~ NSML_AGL_C9_DSTA
#2360
Write 7FFFFFFFh to NSML_AGL_Cn_DSTA
END

Fig.51

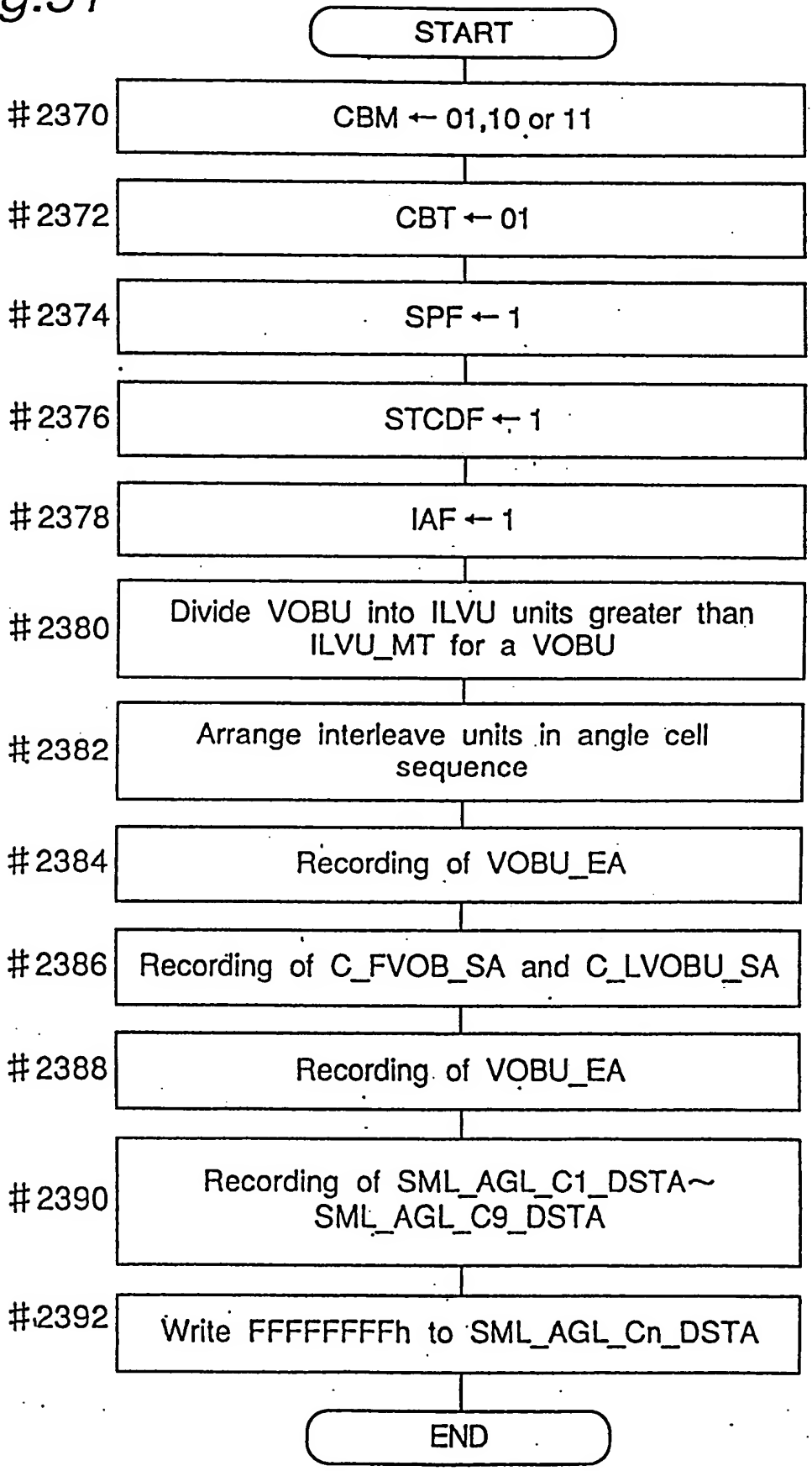

START
#2370
CBM ← 01,10 or 11
#2372
CBT ← 01
#2374
SPF ← 1
#2376
STCDF ← 1
#2378
IAF ← 1
#2380
Divide VOBU into ILVU units greater than ILVU_MT for a VOBU
#2382
Arrange interleave units in angle cell sequence
#2384
Recording of VOBU_EA
#2386
Recording of C_FVOB_SA and C_LVOBU_SA
#2388
Recording of VOBU_EA
#2390
Recording of SML_AGL_C1_DSTA~ SML_AGL_C9_DSTA
#2392
Write FFFFFFFFh to SML_AGL_Cn_DSTA
END

Fig.52

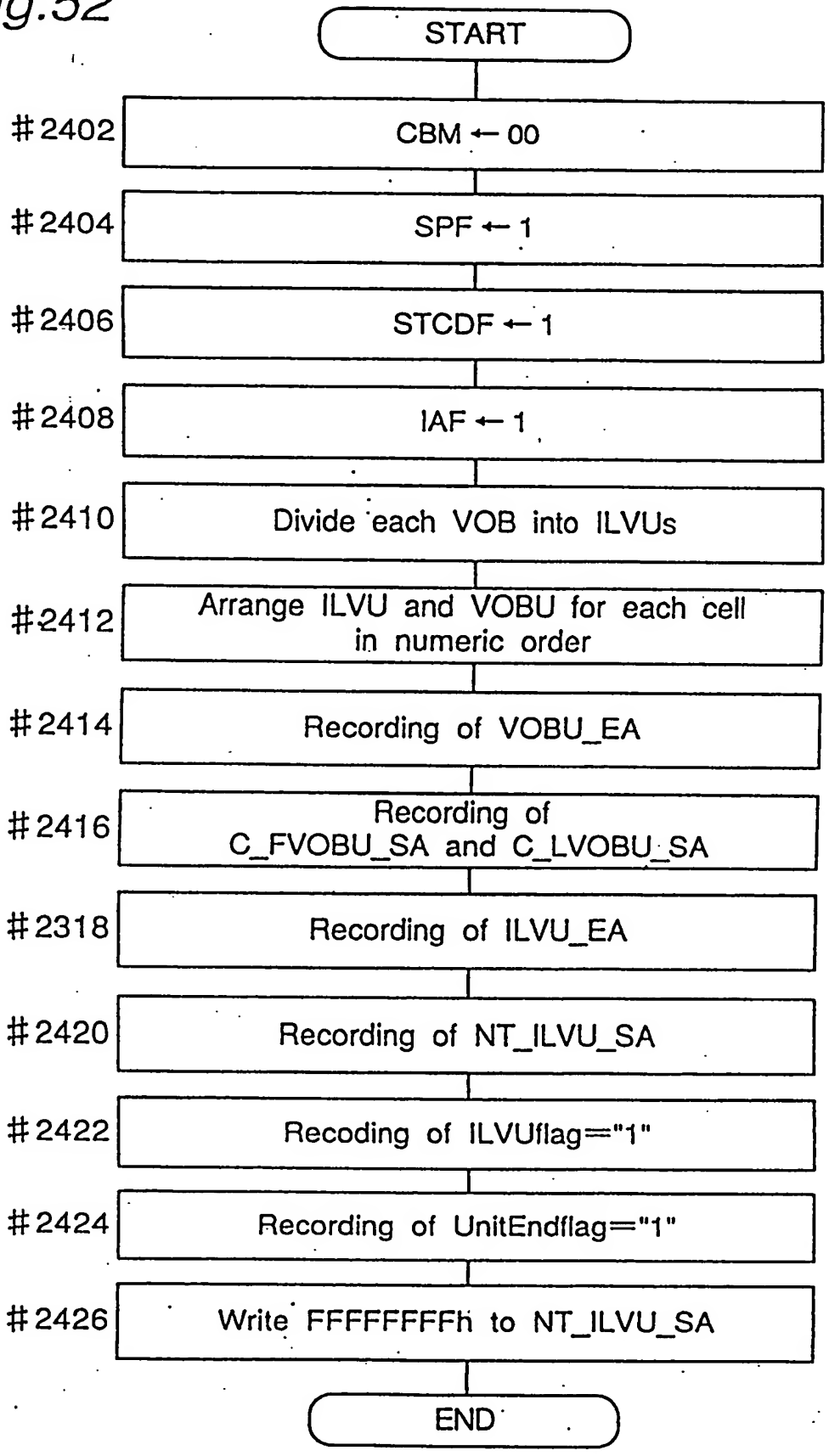

START
#2402
CBM ← 00
#2404
SPF ← 1
#2406
STCDF ← 1
#2408
IAF ← 1
#2410
Divide each VOB into ILVUs
#2412
Arrange ILVU and VOBU for each cell in numeric order
#2414
Recording of VOBU_EA
#2416
Recording of C_FVOBU_SA and C_LVOBU_SA
#2318
Recording of ILVU_EA
#2420
Recording of NT_ILVU_SA
#2422
Recoding of ILVUflag="1"
#2424
Recording of UnitEndflag="1"
#2426
Write FFFFFFFFh to NT_ILVU_SA
END

Fig.53

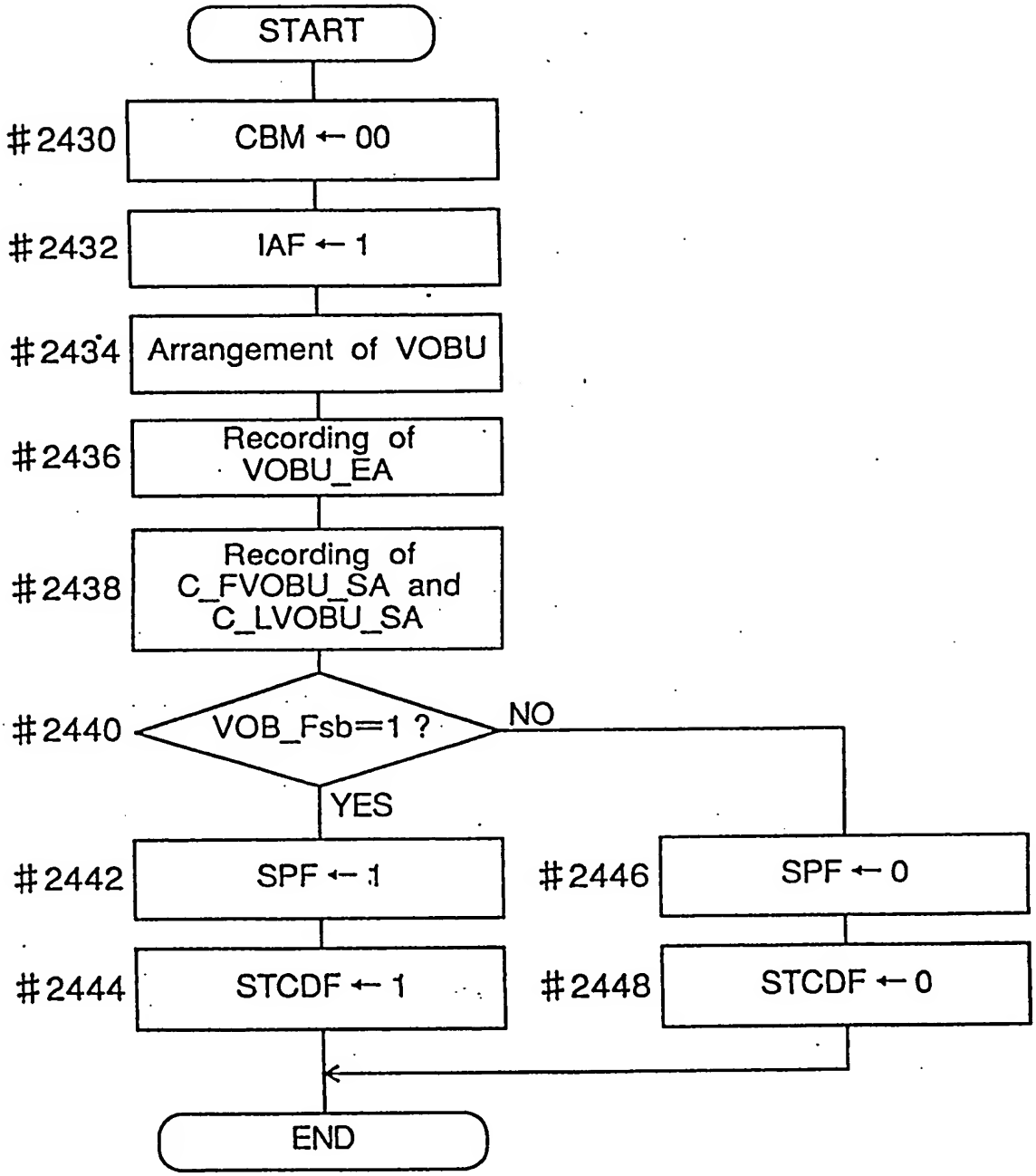

START
#2430
CBM ← 00
#2432
IAF ← 1
#2434
Arrangement of VOBU
#2436
Recording of VOBU_EA
#2438
Recording of C_FVOBU_SA and C_LVOBU_SA
#2440
VOB_Fsb=1 ?
NO
YES
#2442
SPF ← 1
#2444
STCDF ← 1
#2446
SPF ← 0
#2448
STCDF ← 0
END

Fig.54

Scenario info. register	Register Name
	Angle No. (ANGLE_NO_reg)
	VTS No. (VTS_NO_reg)
	PGC No. (VTS_PGCI_NO_reg)
	Audio ID (AUDIO_ID_reg).
	Sub-picture ID (SP_ID_reg)
	SCR buffer (SCR_buffer)

Cell information register	Register Name	Value
	Cell block mode (CBM_reg)	N_BLOCK: Not a Cell in the block
		F_CELL: First Cell in the block
		BLOCK: Cell in the block
		L_CELL: Last Cell in the block
	Cellblock type (CBT_reg)	N_BLOCK: Not a part of in the block
		A_BLOCK: Angle block
	Seamless reproduction flag (SPF_reg)	SML: A Cell shall be presented seamlessly
		NSML: A Cell shall not be presented seamlessly
	Interleave allocation flag (IAF_reg)	N_ILVB: Exist in the Contiguous block
		ILVB: Exist in the Interleaved block
	STC re-setting flag (STCDF_reg)	STC_NRESET: STC reset is not necessary
		STC_RESET: STC reset is necessary
	Seamless angle switching flag (SACF_reg)	SML: A Cell shall be presented seamlessly
		NSML: A Cell shall not be presented seamlessly
	Starting address of first VOBU in cell (C_FVOBU_SA_reg)	
	Starting address of last VOBU in cell (C_LVOBU_SA_reg)	

Fig.55

Information registers for Non-seamless multi-angle control	Register Name	
	N.A.N.A. 1 (NSML_AGL_C1_DSTA_reg)	
	N.A.N.A. 2 (NSML_AGL_C2_DSTA_reg)	
	N.A.N.A. 3 (NSML_AGL_C3_DSTA_reg)	
	N.A.N.A. 4 (NSML_AGL_C4_DSTA_reg)	
	N.A.N.A. 5 (NSML_AGL_C5_DSTA_reg)	
	N.A.N.A. 6 (NSML_AGL_C6_DSTA_reg)	
	N.A.N.A. 7 (NSML_AGL_C7_DSTA_reg)	
	N.A.N.A. 8 (NSML_AGL_C8_DSTA_reg)	
	N.A.N.A. 9 (NSML_AGL_C9_DSTA_reg)	
Information registers for seamless multi-angle control	Register Name	
	S.A.S.A. 1 (SML_AGL_C1_DSTA_reg)	
	S.A.S.A. 2 (SML_AGL_C2_DSTA_reg)	
	S.A.S.A. 3 (SML_AGL_C3_DSTA_reg)	
	S.A.S.A. 4 (SML_AGL_C4_DSTA_reg)	
	S.A.S.A. 5 (SML_AGL_C5_DSTA_reg)	
	S.A.S.A. 6 (SML_AGL_C6_DSTA_reg)	
	S.A.S.A. 7 (SML_AGL_C7_DSTA_reg)	
	S.A.S.A. 8 (SML_AGL_C8_DSTA_reg)	
	S.A.S.A. 9 (SML_AGL_C9_DSTA_reg)	
VOBU info. Register	Register Name	
	VOBU final address (VOBU_EA_reg)	
Registers for seamless reproduction	Register Name.	Value
	Interleave unit flag (ILVU_flag_reg)	ILVU: VOBU is in ILVU
		N_ILVU: VOBU is not in ILVU
	Unit end flag (UNIT_END_flag_reg)	END: At the end of ILVU
		N_END: Not at the end of ILVU
	Final pack address of ILVU (ILVU_EA_reg)	
	Starting address of next ILVU (NT_ILVU_SA_reg)	
	I. V. F. P. S. T. (VOB_V_SPTM_reg)	
	F. V. F. P. T. T. (VOB_V_EPTM_reg)	
	Audio reproduction stopping time 1 (VOB_A_STP_PTM1_reg)	
	Audio reproduction stopping time 2 (VOB_A_STP_PTM2_reg)	
	Audio reproduction stopping period 1 (VOB_A_GAP_LEN1_reg)	
	Audio reproduction stopping period 2 (VOB_A_GAP_LEN2_reg)	

Fig.56

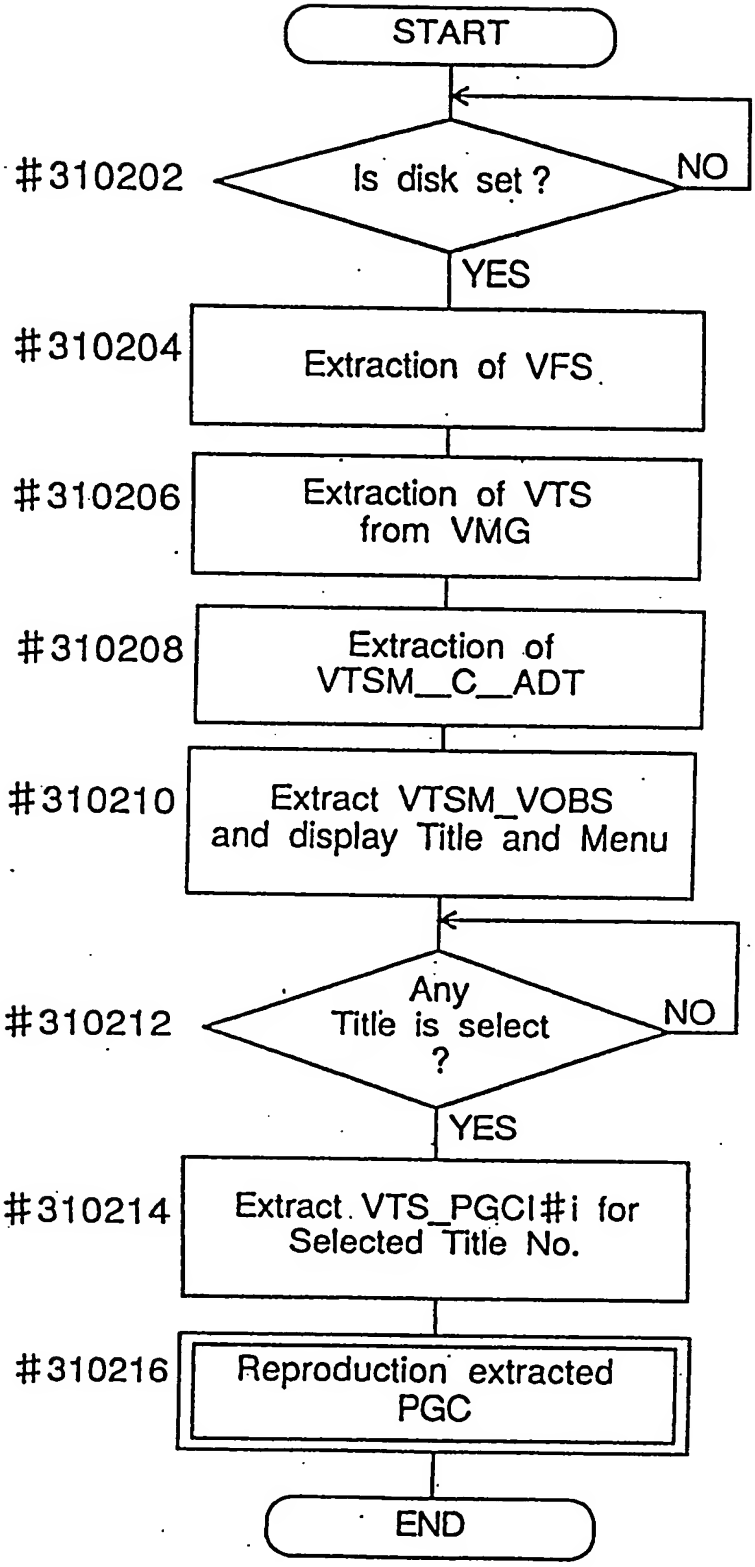

START
#310202
Is disk set ?
NO
YES
#310204
Extraction of VFS
#310206
Extraction of VTS from VMG
#310208
Extraction of VTSM_C_ADT
#310210
Extract VTSM_VOBS and display Title and Menu
#310212
Any Title is select ?
NO
YES
#310214
Extract VTS_PGCI#i for Selected Title No.
#310216
Reproduction extracted PGC
END

Fig.57

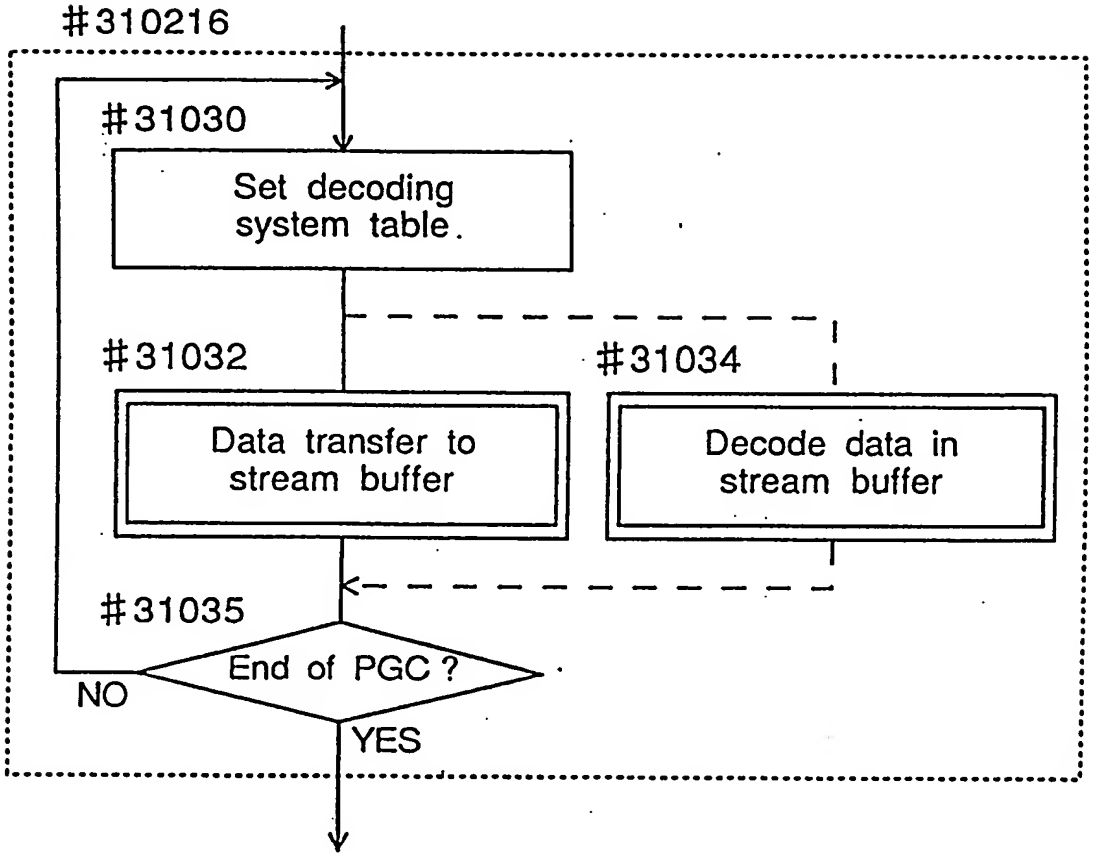

#310216
#31030
Set decoding system table.
#31032
Data transfer to stream buffer
#31034
Decode data in stream buffer
#31035
End of PGC ?
NO
YES

Fig.58

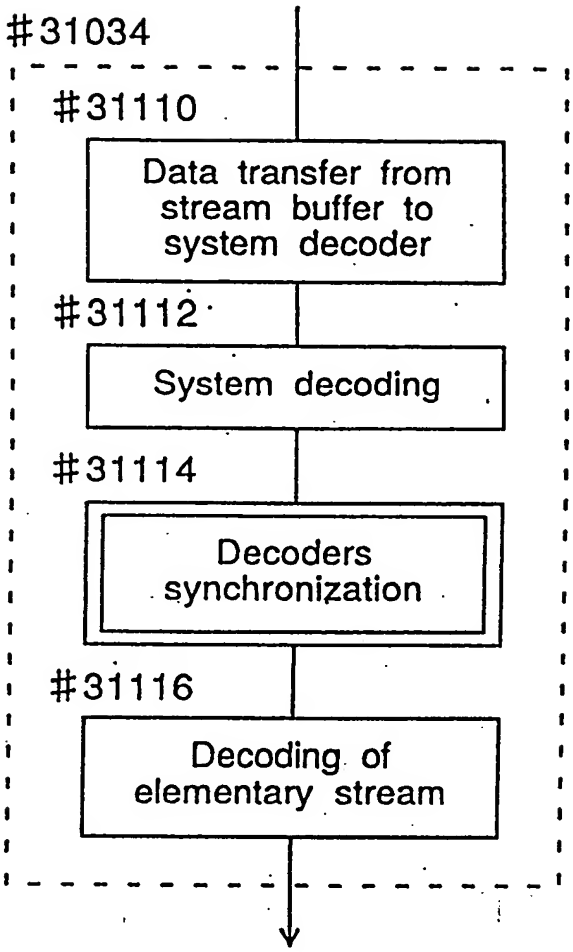

#31034
#31110
Data transfer from stream buffer to system decoder
#31112
System decoding
#31114
Decoders synchronization
#31116
Decoding of elementary stream

Fig.59

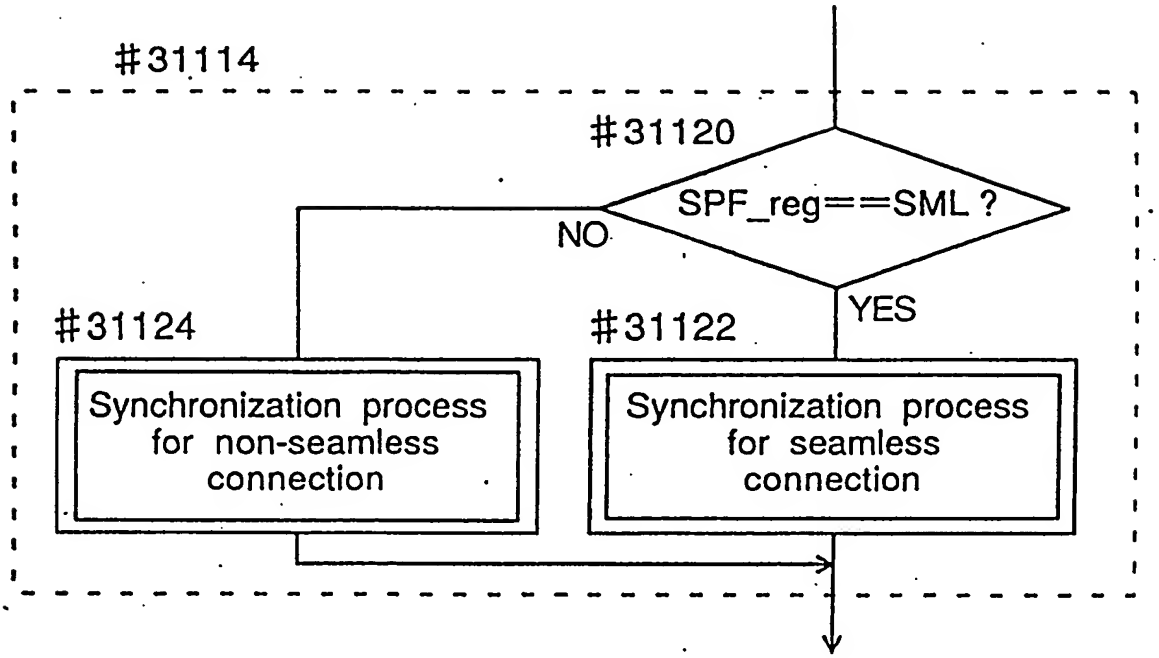

#31114
#31120
SPF_reg==SML ?
NO
YES
#31124
#31122
Synchronization process for non-seamless connection
Synchronization process for seamless connection

Fig.60

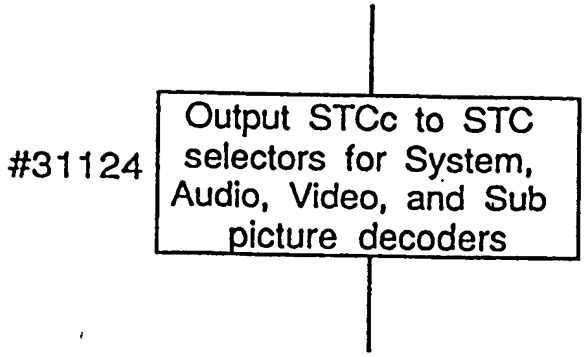

#31124
Output STCc to STC selectors for System, Audio, Video, and Sub picture decoders

Fig.61

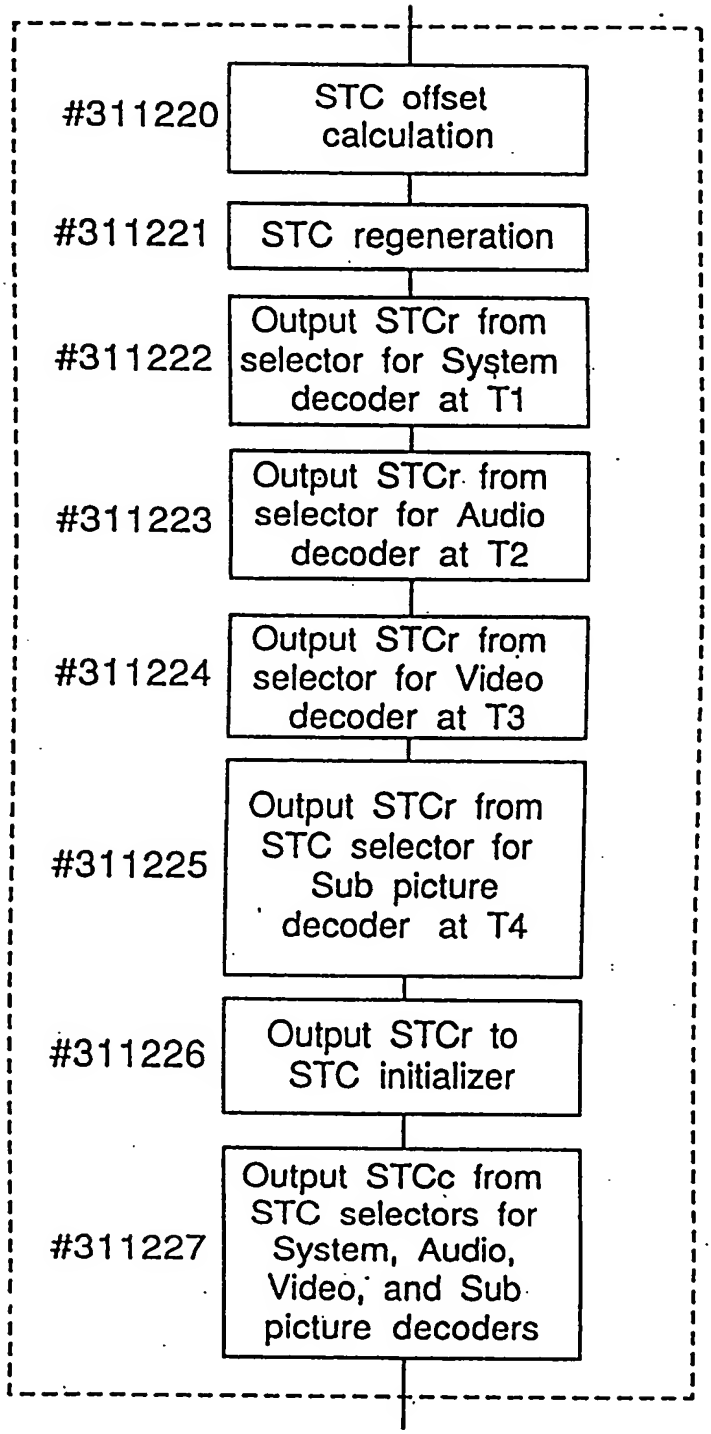

#311220
STC offset calculation
#311221
STC regeneration
#311222
Output STCr from selector for System decoder at T1
#311223
Output STCr from selector for Audio decoder at T2
#311224
Output STCr from selector for Video decoder at T3
#311225
Output STCr from STC selector for Sub picture decoder at T4
#311226
Output STCr to STC initializer
#311227
Output STCc from STC selectors for System, Audio, Video, and Sub picture decoders

Fig.62

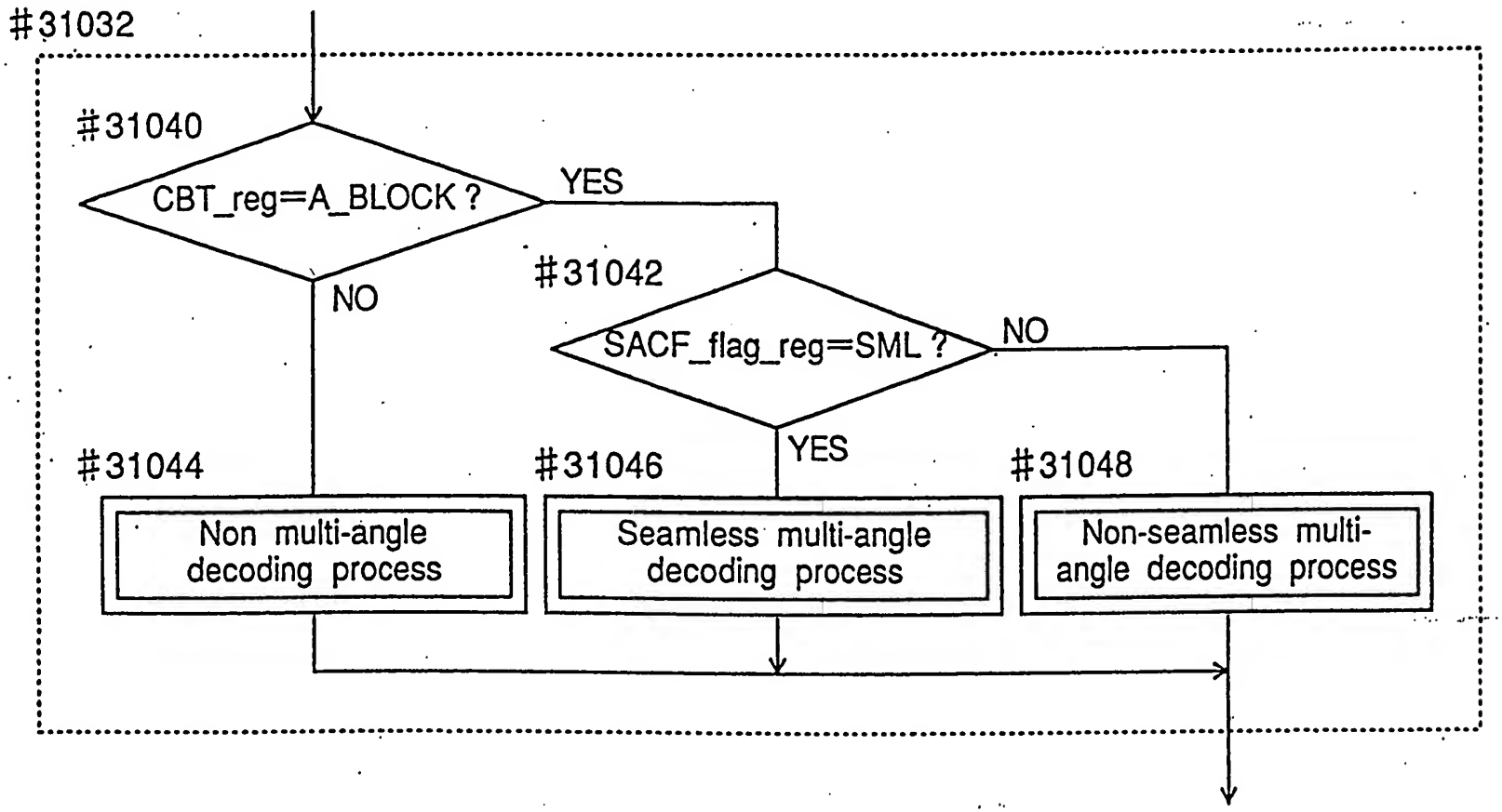

#31032
#31040
CBT_reg=A_BLOCK ?
YES
NO
#31042
SACF_flag_reg=SML ?
NO
YES
#31044
#31046
#31048
Non multi-angle decoding process
Seamless multi-angle decoding process
Non-seamless multi-angle decoding process

Fig.63

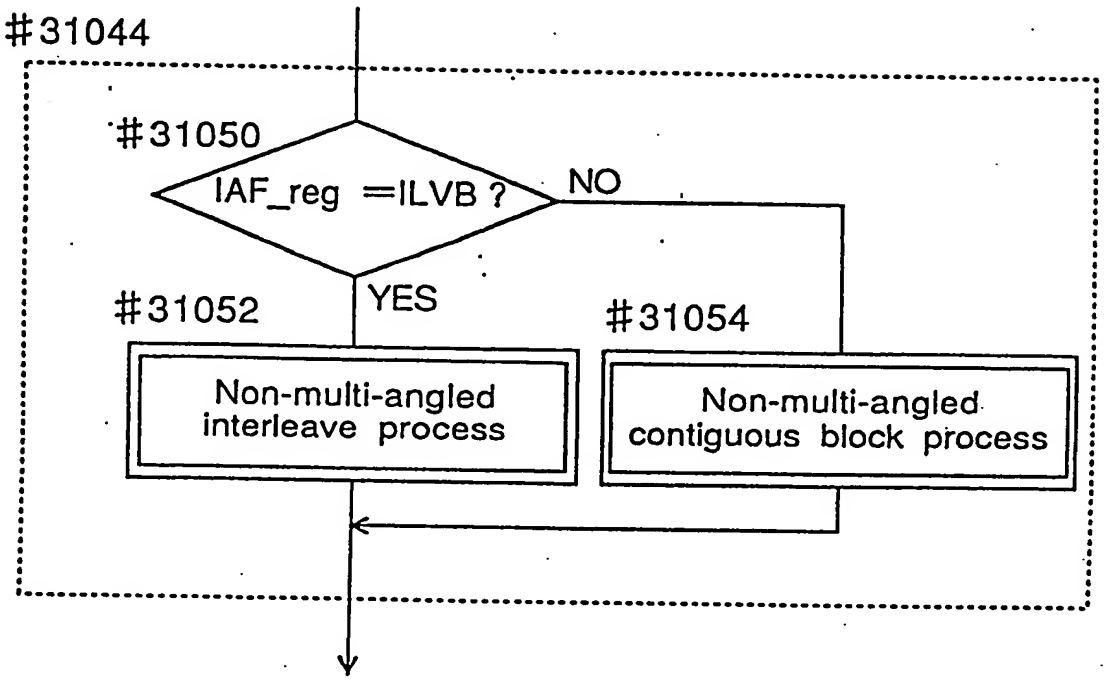

#31044
#31050
IAF_reg =ILVB ?
NO
YES
#31052
#31054
Non-multi-angled interleave process
Non-multi-angled contiguous block process

Fig.64

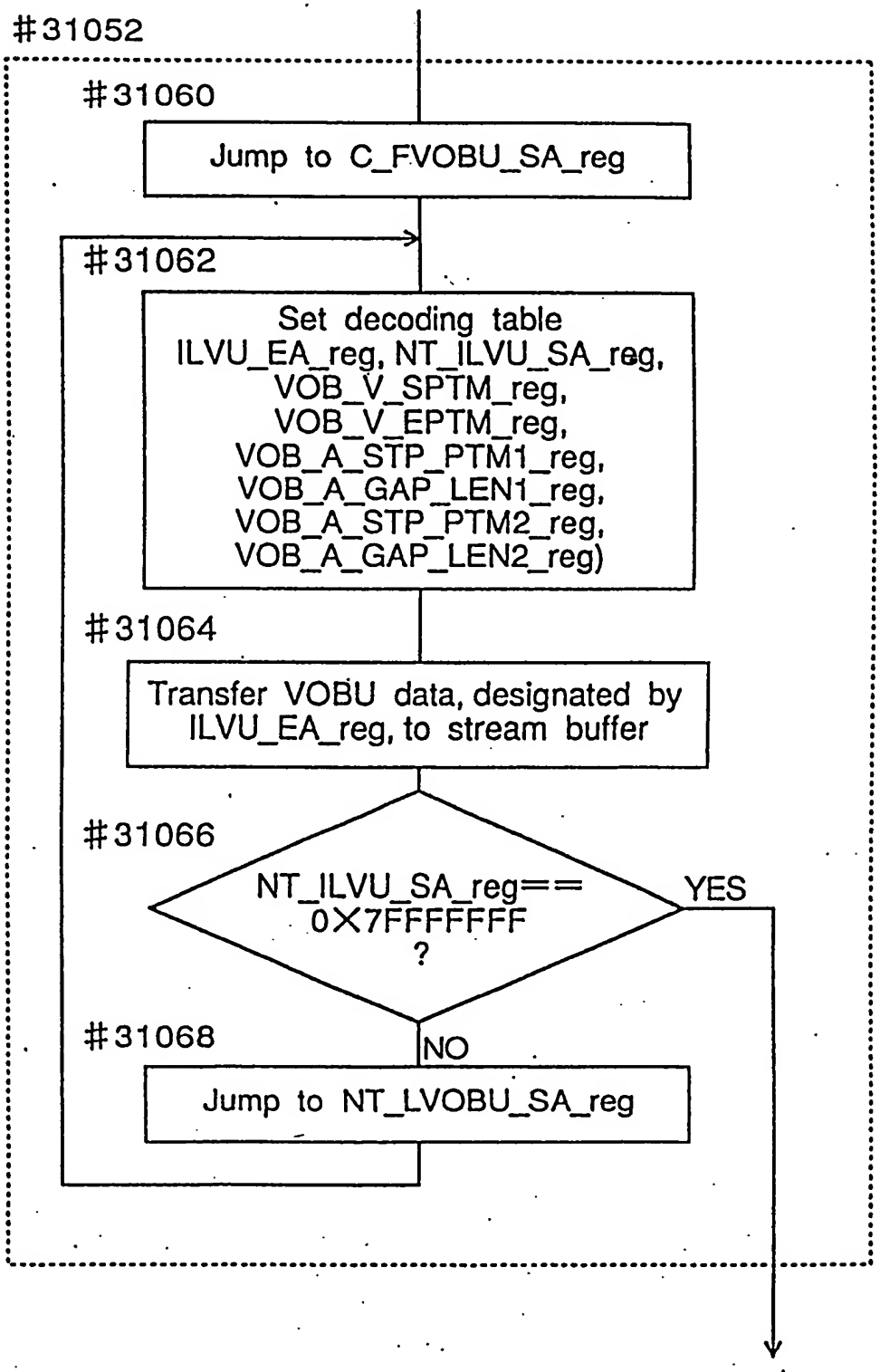

#31052
#31060
Jump to C_FVOBU_SA_reg
#31062
Set decoding table
ILVU_EA_reg, NT_ILVU_SA_reg,
VOB_V_SPTM_reg,
VOB_V_EPTM_reg,
VOB_A_STP_PTM1_reg,
VOB_A_GAP_LEN1_reg,
VOB_A_STP_PTM2_reg,
VOB_A_GAP_LEN2_reg)
#31064
Transfer VOBU data, designated by
ILVU_EA_reg, to stream buffer
#31066
NT_ILVU_SA_reg==
0X7FFFFFFF
?
YES
NO
#31068
Jump to NT_LVOBU_SA_reg

Fig.65

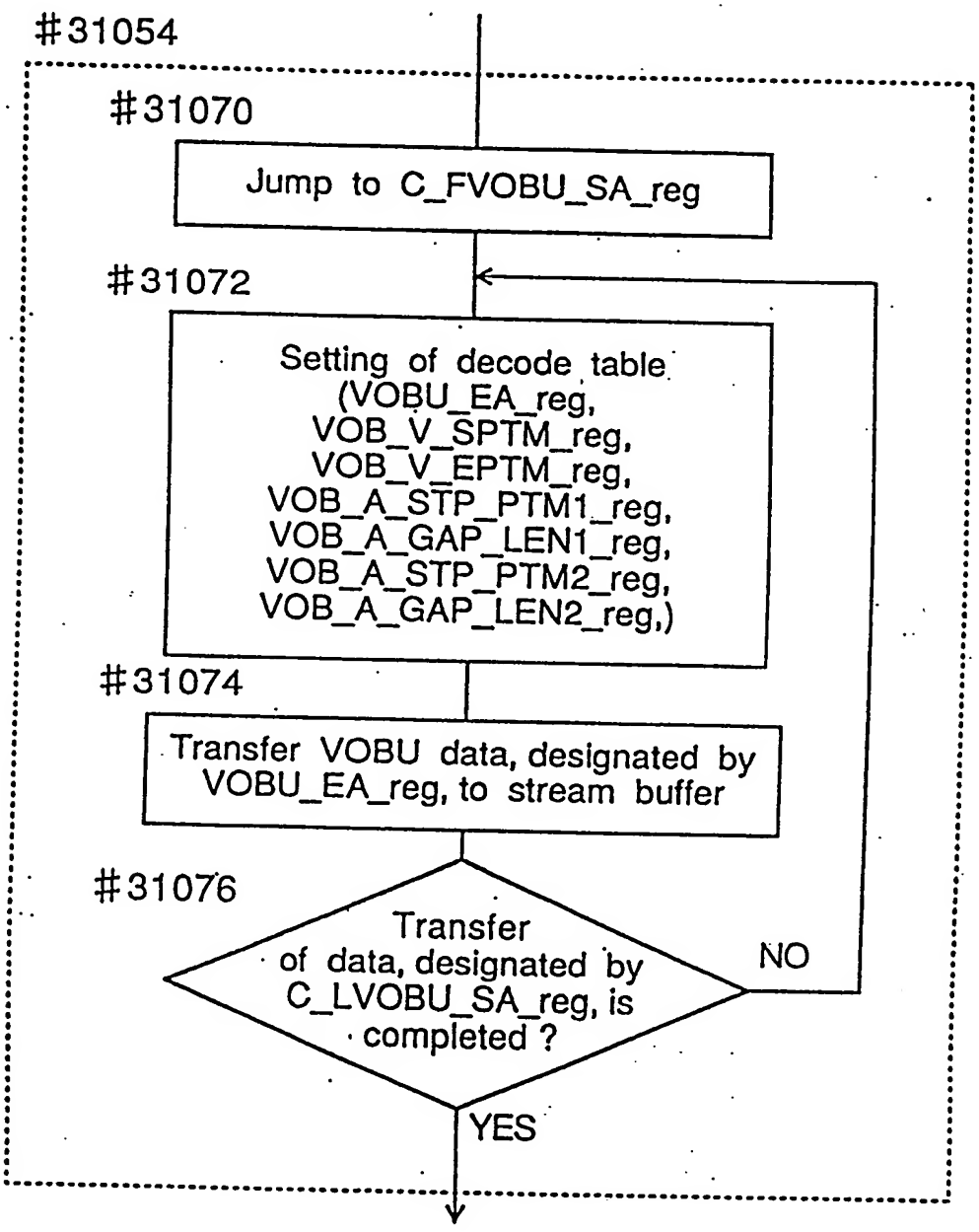

#31054
#31070
Jump to C_FVOBU_SA_reg
#31072
Setting of decode table
(VOBU_EA_reg,
VOB_V_SPTM_reg,
VOB_V_EPTM_reg,
VOB_A_STP_PTM1_reg,
VOB_A_GAP_LEN1_reg,
VOB_A_STP_PTM2_reg,
VOB_A_GAP_LEN2_reg,)
#31074
Transfer VOBU data, designated by VOBU_EA_reg, to stream buffer
#31076
Transfer of data, designated by C_LVOBU_SA_reg, is completed ?
NO
YES

Fig.66

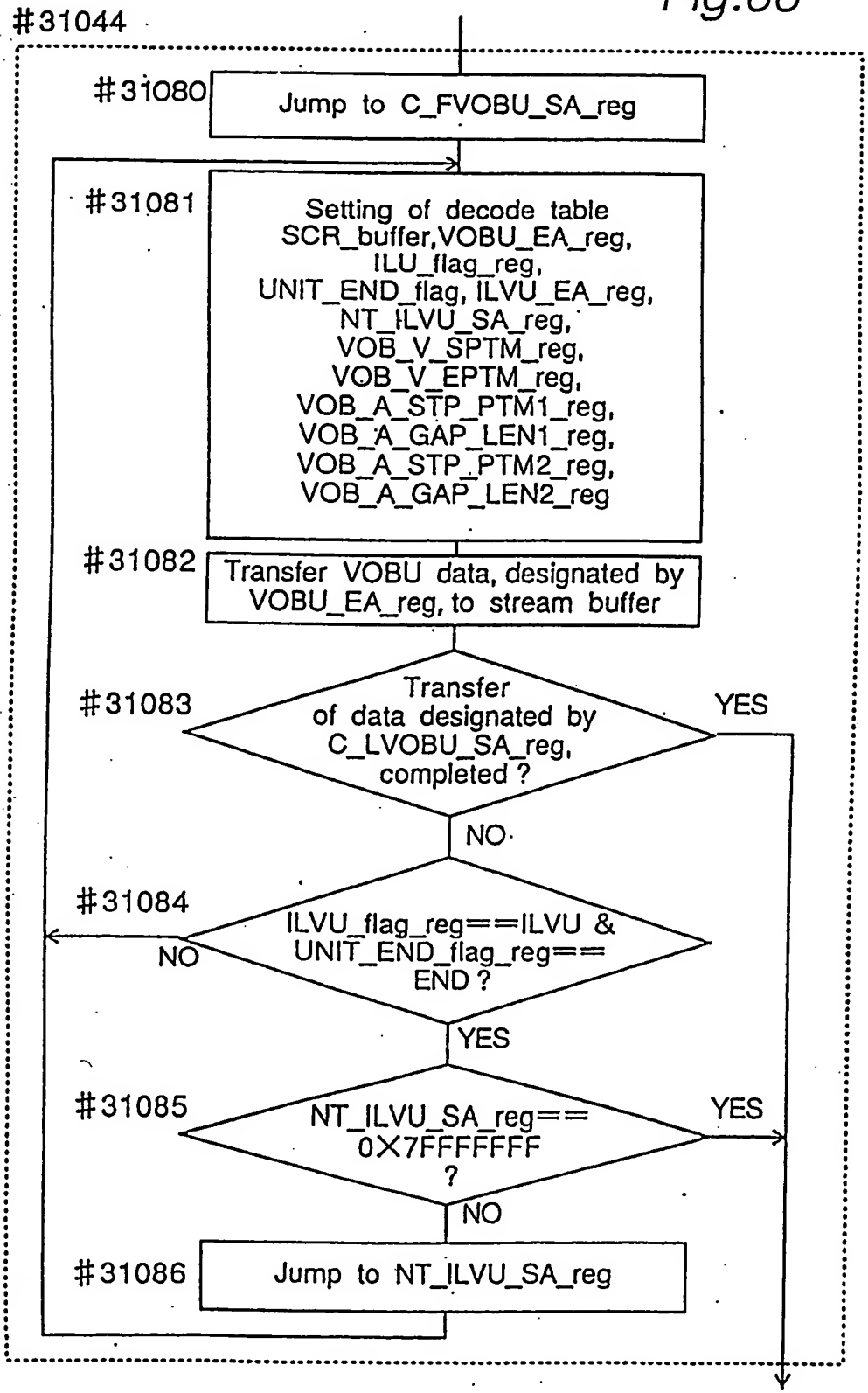

#31044
#31080
Jump to C_FVOBU_SA_reg
#31081
Setting of decode table
SCR_buffer,VOBU_EA_reg,
ILU_flag_reg,
UNIT_END_flag, ILVU_EA_reg,
NT_ILVU_SA_reg,
VOB_V_SPTM_reg,
VOB_V_EPTM_reg,
VOB_A_STP_PTM1_reg,
VOB_A_GAP_LEN1_reg,
VOB_A_STP_PTM2_reg,
VOB_A_GAP_LEN2_reg
#31082
Transfer VOBU data, designated by VOBU_EA_reg, to stream buffer
#31083
Transfer of data designated by C_LVOBU_SA_reg, completed ?
YES
NO
#31084
ILVU_flag_reg==ILVU & UNIT_END_flag_reg== END ?
NO
YES
#31085
NT_ILVU_SA_reg== 0X7FFFFFFF ?
YES
NO
#31086
Jump to NT_ILVU_SA_reg

Fig.67

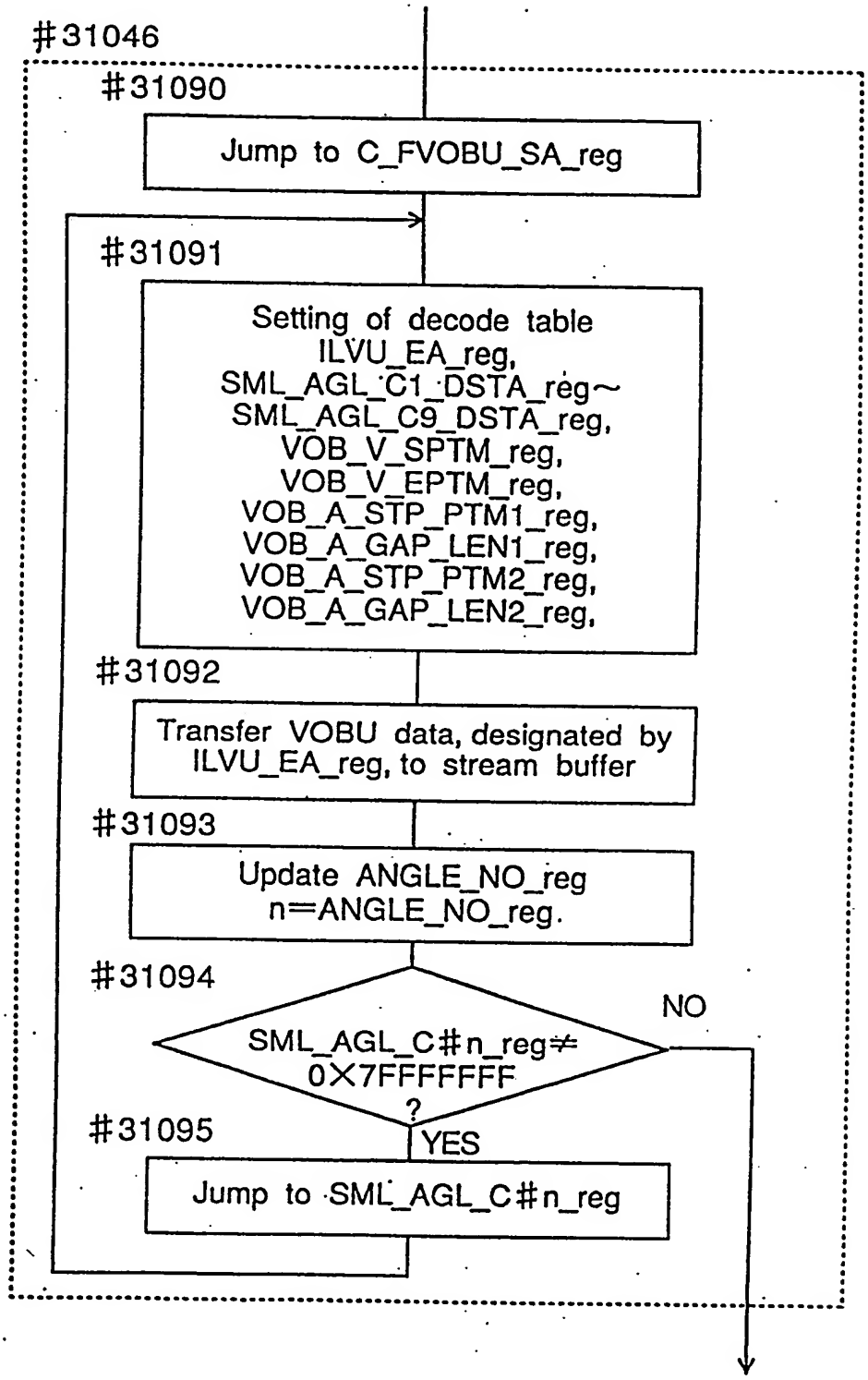

#31046
#31090
Jump to C_FVOBU_SA_reg
#31091
Setting of decode table
ILVU_EA_reg,
SML_AGL_C1_DSTA_reg~
SML_AGL_C9_DSTA_reg,
VOB_V_SPTM_reg,
VOB_V_EPTM_reg,
VOB_A_STP_PTM1_reg,
VOB_A_GAP_LEN1_reg,
VOB_A_STP_PTM2_reg,
VOB_A_GAP_LEN2_reg,
#31092
Transfer VOBU data, designated by
ILVU_EA_reg, to stream buffer
#31093
Update ANGLE_NO_reg
n=ANGLE_NO_reg.
#31094
SML_AGL_C#n_reg≠
0X7FFFFFFF
?
NO
YES
#31095
Jump to SML_AGL_C#n_reg

Fig.68

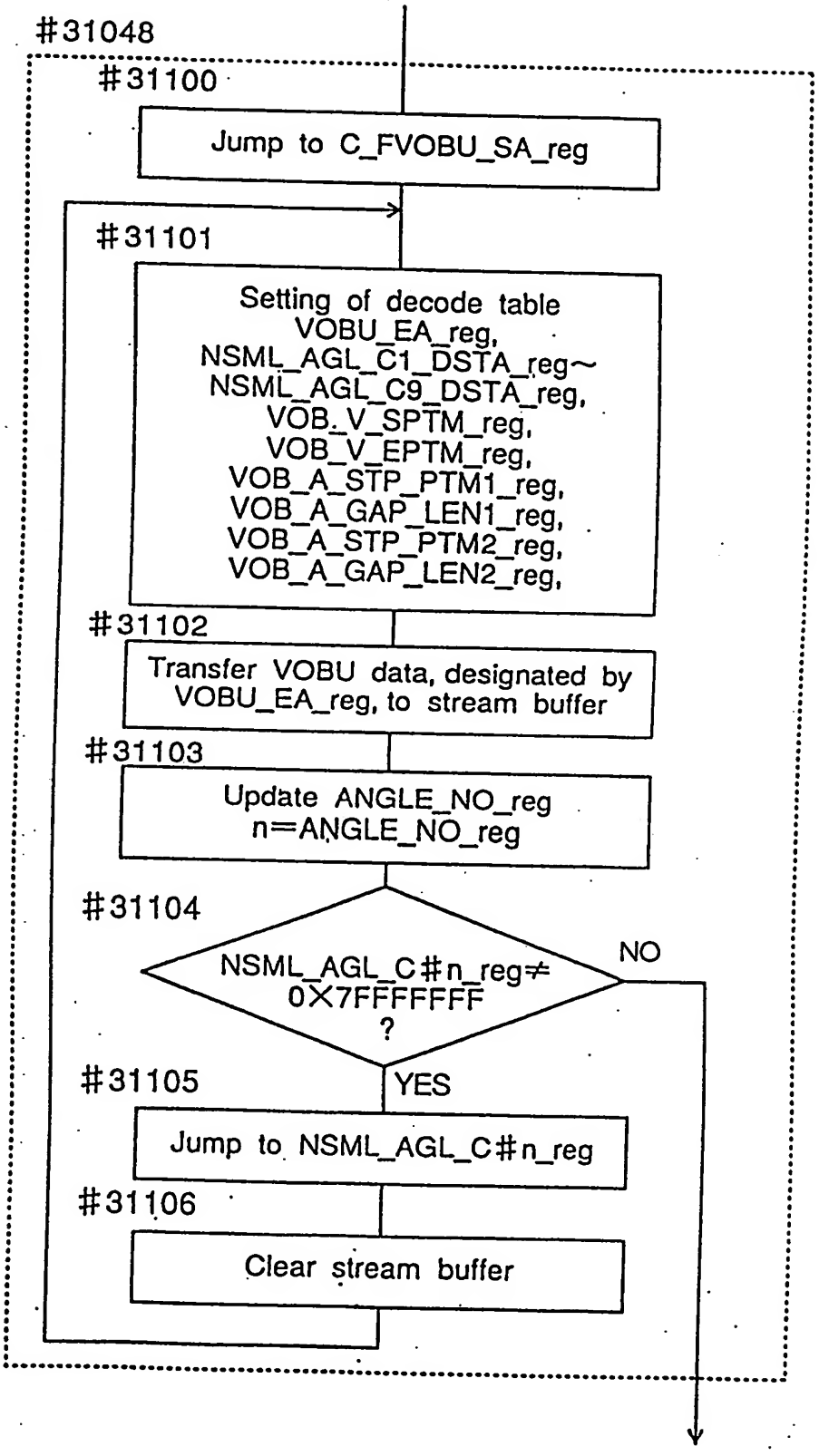

#31048
#31100
Jump to C_FVOBU_SA_reg
#31101
Setting of decode table
VOBU_EA_reg,
NSML_AGL_C1_DSTA_reg~
NSML_AGL_C9_DSTA_reg,
VOB_V_SPTM_reg,
VOB_V_EPTM_reg,
VOB_A_STP_PTM1_reg,
VOB_A_GAP_LEN1_reg,
VOB_A_STP_PTM2_reg,
VOB_A_GAP_LEN2_reg,
#31102
Transfer VOBU data, designated by VOBU_EA_reg, to stream buffer
#31103
Update ANGLE_NO_reg
n=ANGLE_NO_reg
#31104
NSML_AGL_C#n_reg≠
0X7FFFFFFF
?
NO
YES
#31105
Jump to NSML_AGL_C#n_reg
#31106
Clear stream buffer

Fig.69

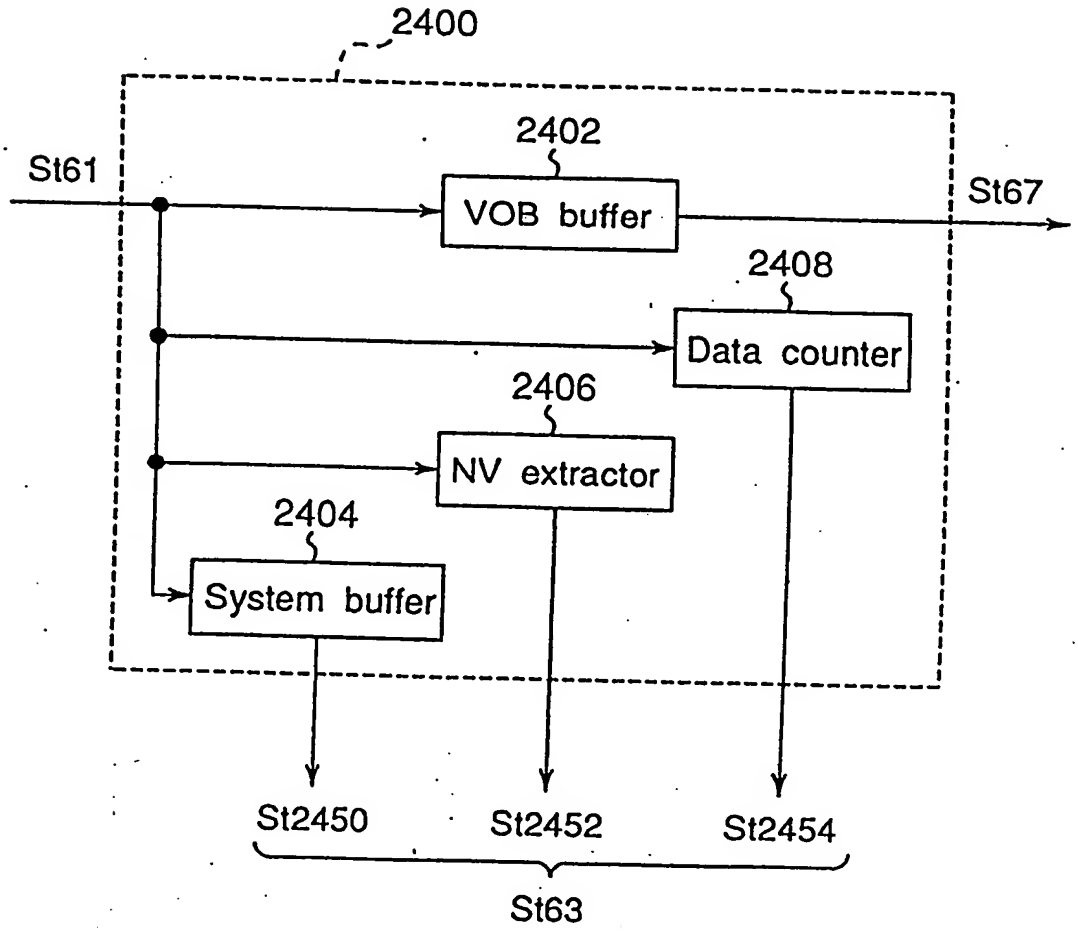

2400
2402
St61
VOB buffer
St67
2408
Data counter
2406
NV extractor
2404
System buffer
St2450
St2452
St2454
St63

Fig.70

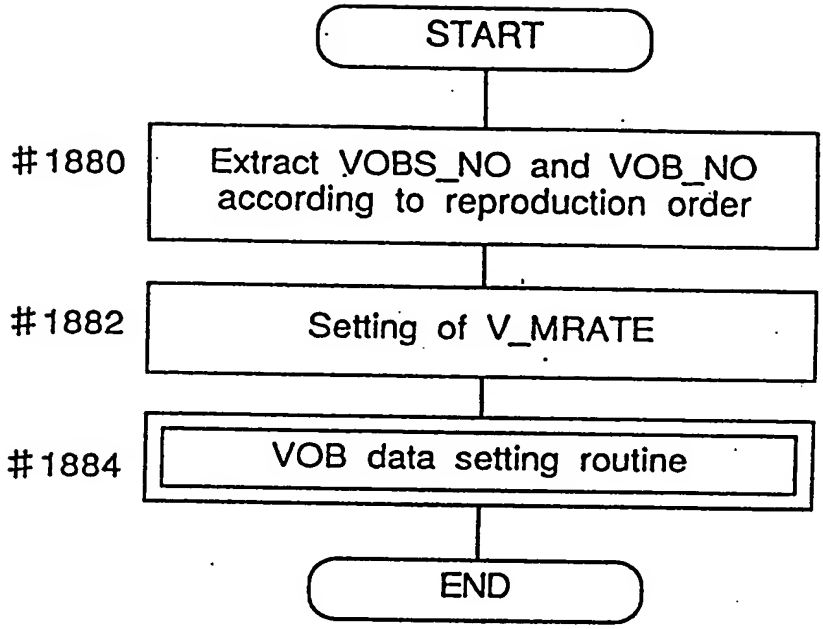

START
#1880
Extract VOBS_NO and VOB_NO according to reproduction order
#1882
Setting of V_MRATE
#1884
VOB data setting routine
END

Fig.71

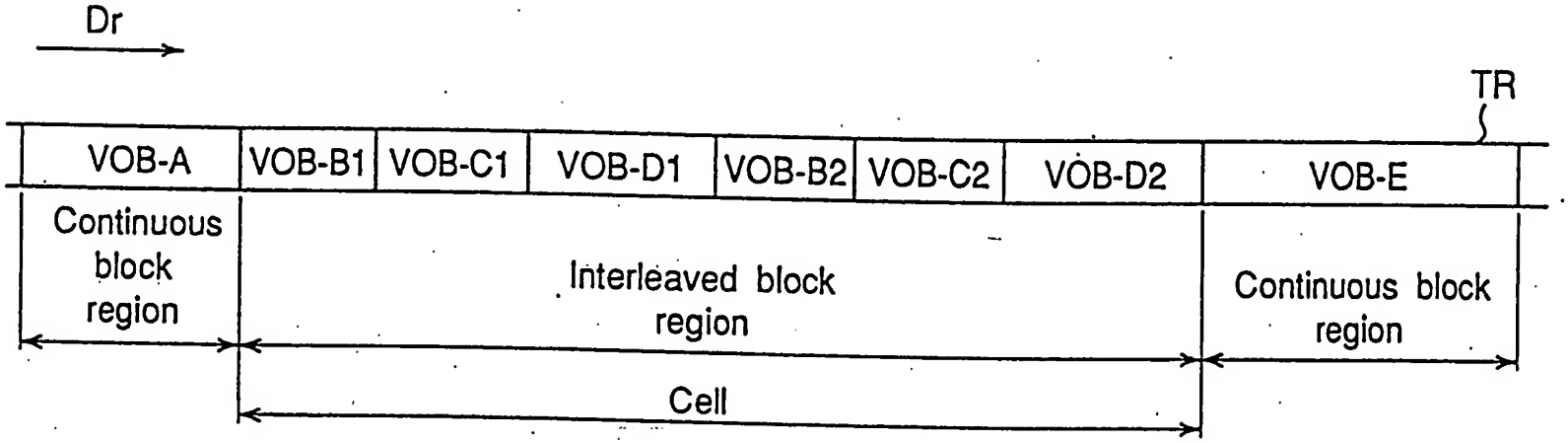

Dr
TR
VOB-A
VOB-B1
VOB-C1
VOB-D1
VOB-B2
VOB-C2
VOB-D2
VOB-E
Continuous block region
Interleaved block region
Continuous block region
Cell

Fig.72

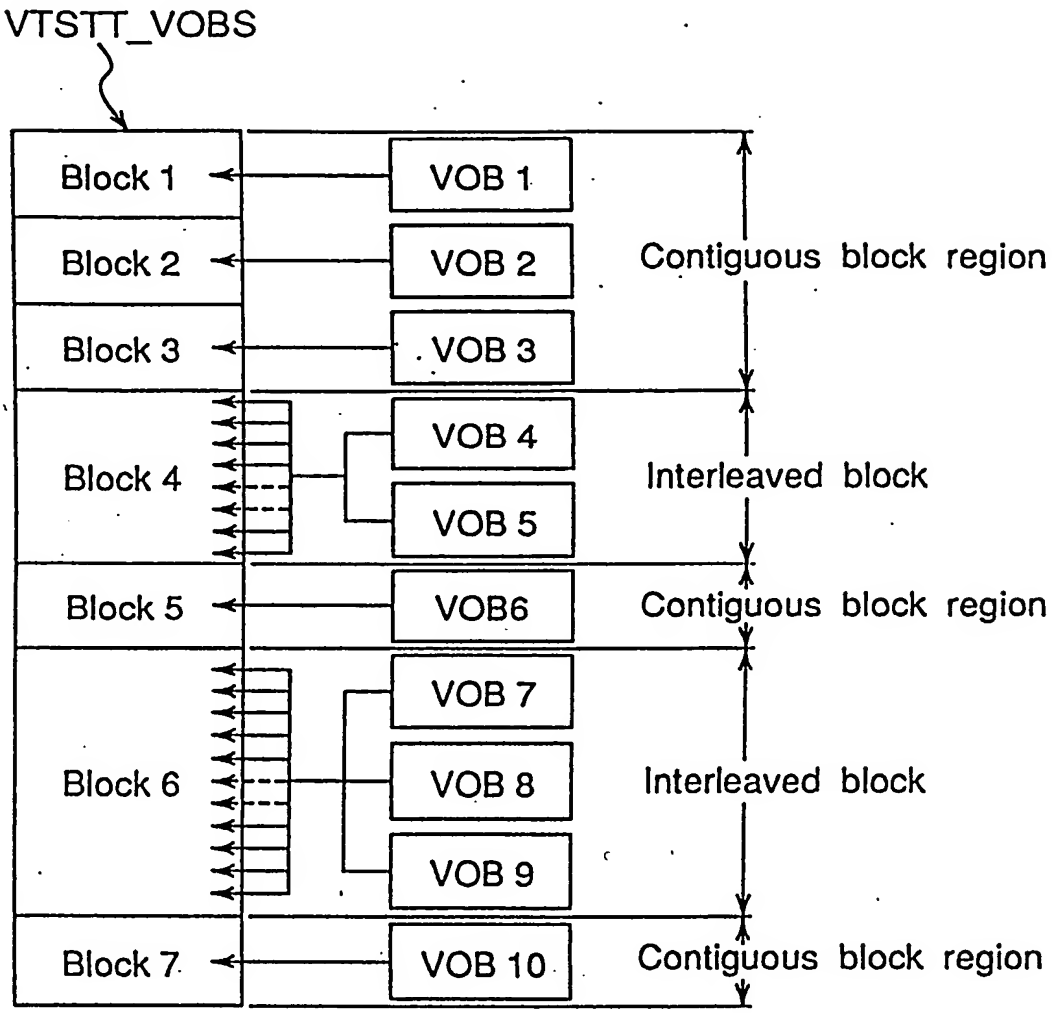

VTSTT_VOBS
Block 1
Block 2
Block 3
Block 4
Block 5
Block 6
Block 7
VOB 1
VOB 2
VOB 3
VOB 4
VOB 5
VOB6
VOB 7
VOB 8
VOB 9
VOB 10
Contiguous block region
Interleaved block
Contiguous block region
Interleaved block
Contiguous block region

Fig.73

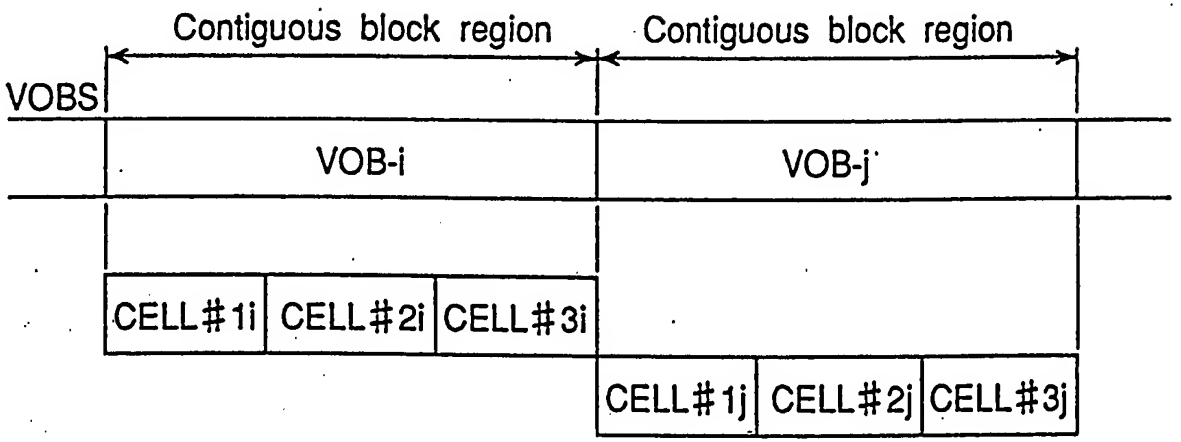
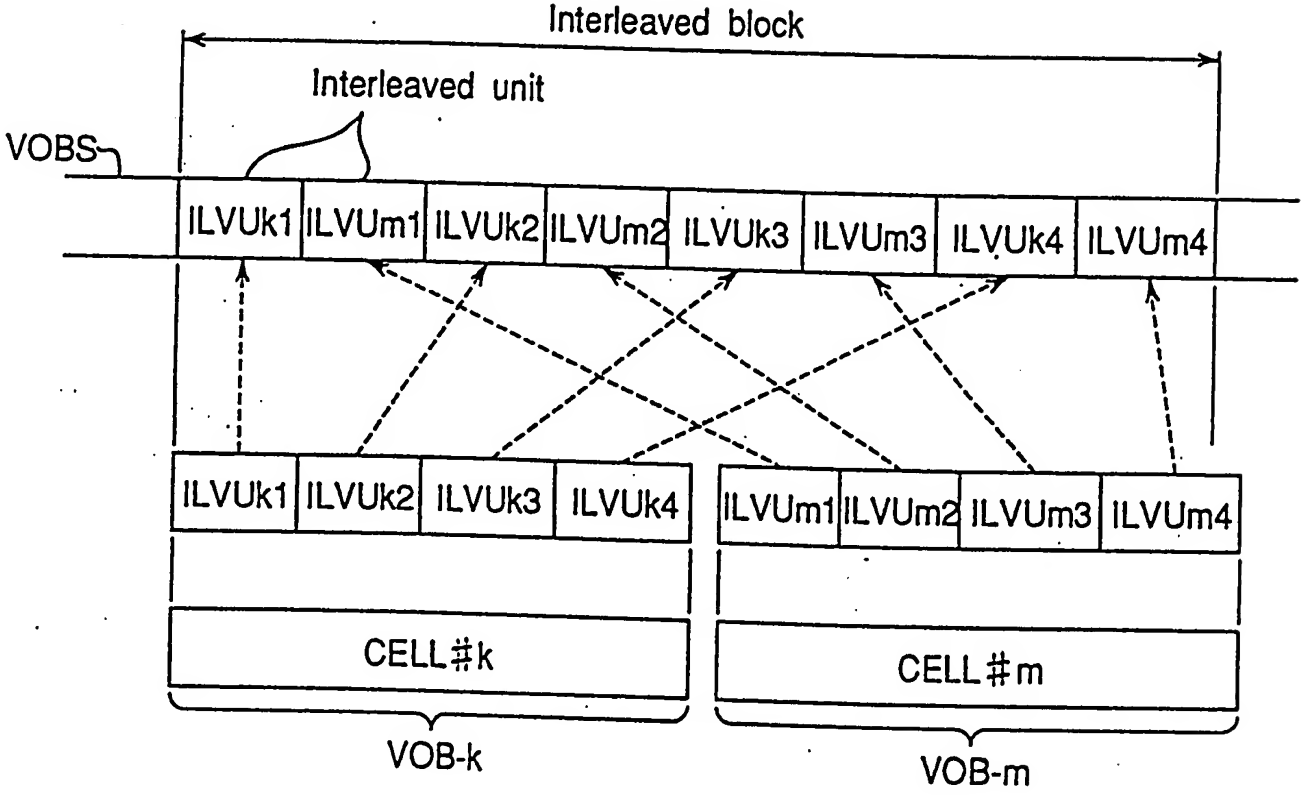

Contiguous block region
Contiguous block region
VOBS
VOB-i
VOB-j
CELL#1i
CELL#2i
CELL#3i
CELL#1j
CELL#2j
CELL#3j

Fig.74


Interleaved block
Interleaved unit
VOBS
ILVUk1
ILVUm1
ILVUk2
ILVUm2
ILVUk3
ILVUm3
ILVUk4
ILVUm4
ILVUk1
ILVUk2
ILVUk3
ILVUk4
ILVUm1
ILVUm2
ILVUm3
ILVUm4
CELL#k
CELL#m
VOB-k
VOB-m